Refine Search

Search Results -

Terms	Documents
(706/21).ccls. and electronic near commerce near system	1

Database: US Pre-Grant Publication Full-Text Database; US Patents Full-Text Database; US OCR Full-Text Database; EPO Abstracts Database; JPO Abstracts Database; Derwent World Patents Index; IBM Technical Disclosure Bulletins

Search: L3

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Tuesday, April 12, 2005 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR			
L3	706/21.ccls. and electronic near commerce near system	1	L3
L2	L1 and electronic near commerce near data	81	L2
L1	electronic near commerce near system	2227	L1

END OF SEARCH HISTORY

Hit List

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 1 through 1 of 1 returned.

☐ 1. Document ID: US 20030140023 A1

Using default format because multiple data bases are involved.

L3: Entry 1 of 1 File: PGPB Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030140023
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030140023 A1

TITLE: System and method for pre-processing input data to a non-linear model for use in electronic commerce

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ferguson, Bruce	Round Rock	TX	US	
Hartman, Eric	Austin	TX	US	

US-CL-CURRENT: 706/21; 706/15

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
(706/21).ccls. and electronic near commerce near system	1

Display Format: - Change Format

Previous Page Next Page Go to Doc#

Refine Search

Search Results -

Terms	Documents
L1 and electronic near commerce near data	81

Database: US Pre-Grant Publication Full-Text Database; US Patents Full-Text Database; US OCR Full-Text Database; EPO Abstracts Database; JPO Abstracts Database; Derwent World Patents Index; IBM Technical Disclosure Bulletins

Search: L2

Refine Search | Recall Text | Clear | Interrupt

Search History

DATE: Tuesday, April 12, 2005 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR			
L2	L1 and electronic near commerce near data	81	L2
L1	electronic near commerce near system	2227	L1

END OF SEARCH HISTORY

Hit List

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 1 through 50 of 81 returned.

☐ 1. Document ID: US 20050044006 A1

Using default format because multiple data bases are involved.

L2: Entry 1 of 81 File: PGPB Feb 24, 2005

PGPUB-DOCUMENT-NUMBER: 20050044006
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050044006 A1

TITLE: Electronic commerce system using mobile terminal and electronic commerce method

PUBLICATION-DATE: February 24, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Soga, Kenji	Tokyo		JP	
Takeuchi, Shohei	Tokyo		JP	
Yano, Yukiko	Tokyo		JP	

US-CL-CURRENT: 705/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 2. Document ID: US 20040253966 A1

L2: Entry 2 of 81 File: PGPB Dec 16, 2004

PGPUB-DOCUMENT-NUMBER: 20040253966
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040253966 A1

TITLE: Networked service providers spontaneously respond and prepared to fulfill user's location-dependent requests

PUBLICATION-DATE: December 16, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lin, Bo-In	Los Altos Hills	CA	US	

US-CL-CURRENT: 455/456.5; 455/456.1

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 3. Document ID: US 20040133793 A1

L2: Entry 3 of 81 File: PGPB Jul 8, 2004

PGPUB-DOCUMENT-NUMBER: 20040133793
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040133793 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: July 8, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Sunnyvale	CA	US	

US-CL-CURRENT: 713/193

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 4. Document ID: US 20040117255 A1

L2: Entry 4 of 81 File: PGPB Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117255
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117255 A1

TITLE: Interactive electronic commerce and message interchange system featuring delivery of messages tailored to individual users

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Nemirofsky, Frank Robert	Alamo	CA	US	
Lincoln, Larry A.	Milpitas	CA	US	

US-CL-CURRENT: 705/14

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 5. Document ID: US 20040117254 A1

L2: Entry 5 of 81 File: PGPB Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117254
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117254 A1

TITLE: Interactive electronic commerce and message interchange system

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Nemirofsky, Frank Robert	Alamo	CA	US	
Lincoln, Larry A.	Milpitas	CA	US	

US-CL-CURRENT: 705/14

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 6. Document ID: US 20040107125 A1

L2: Entry 6 of 81 File: PGPB Jun 3, 2004

PGPUB-DOCUMENT-NUMBER: 20040107125
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040107125 A1

TITLE: Business alliance identification in a web architecture

PUBLICATION-DATE: June 3, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Guheen, Michael F.	Tiburon	CA	US	
Mitchell, James D	Manhattand Beach	CA	US	
Barrese, James J.	San Jose	CA	US	

US-CL-CURRENT: 705/7

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 7. Document ID: US 20040103305 A1

L2: Entry 7 of 81 File: PGPB May 27, 2004

PGPUB-DOCUMENT-NUMBER: 20040103305
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040103305 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: May 27, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Eugene	OR	US	

US-CL-CURRENT: 713/200

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 8. Document ID: US 20040064351 A1

L2: Entry 8 of 81 File: PGPB Apr 1, 2004

PGPUB-DOCUMENT-NUMBER: 20040064351
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040064351 A1

TITLE: Increased visibility during order management in a network-based supply chain environment

PUBLICATION-DATE: April 1, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Mikurak, Michael G.	Gulfport	FL	US	

US-CL-CURRENT: 705/7

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 9. Document ID: US 20040059682 A1

L2: Entry 9 of 81 File: PGPB Mar 25, 2004

PGPUB-DOCUMENT-NUMBER: 20040059682
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040059682 A1

TITLE: Electronic commercial transaction support method

PUBLICATION-DATE: March 25, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hasumi, Yoshitsugu	Saitama		JP	
Kawashima, Takashi	Kanagawa		JP	
Imai, Kazuo	Tokyo		JP	
Hayaashi, Hirofumi	Kanagawa		JP	

US-CL-CURRENT: 705/64

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 10. Document ID: US 20040054630 A1

L2: Entry 10 of 81 File: PGPB Mar 18, 2004

PGPUB-DOCUMENT-NUMBER: 20040054630
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040054630 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: March 18, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Sunnyvale	CA	US	

US-CL-CURRENT: 705/53

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 11. Document ID: US 20040030641 A1

L2: Entry 11 of 81 File: PGPB Feb 12, 2004

PGPUB-DOCUMENT-NUMBER: 20040030641
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040030641 A1

TITLE: Electronic commerce support method

PUBLICATION-DATE: February 12, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Hasumi, Yoshitsugu	Saitama		JP	
Shiotani, Keiji	Chiba		JP	

US-CL-CURRENT: 705/39; 705/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 12. Document ID: US 20030216931 A1

L2: Entry 12 of 81 File: PGPB Nov 20, 2003

PGPUB-DOCUMENT-NUMBER: 20030216931
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030216931 A1

TITLE: Method and system for electronic commerce of semiconductor IP

PUBLICATION-DATE: November 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Moriya, Satoshi	Yokohama-shi		JP	
Kobayashi, Hisayoshi	Tokyo		JP	
Miyazaki, Nobuyuki	Kawagoe-shi		JP	

US-CL-CURRENT: 705/1

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 13. Document ID: US 20030191719 A1

L2: Entry 13 of 81 File: PGPB Oct 9, 2003

PGPUB-DOCUMENT-NUMBER: 20030191719
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030191719 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: October 9, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Sibert, W. Olin	Lexington	MA	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Eugene	OR	US	

US-CL-CURRENT: 705/54

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 14. Document ID: US 20030163431 A1

L2: Entry 14 of 81 File: PGPB Aug 28, 2003

PGPUB-DOCUMENT-NUMBER: 20030163431
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030163431 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: August 28, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Sibert, W. Olin	Lexington	MA	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Eugene	OR	US	

US-CL-CURRENT: 705/64

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 15. Document ID: US 20030149603 A1

L2: Entry 15 of 81 File: PGPB Aug 7, 2003

PGPUB-DOCUMENT-NUMBER: 20030149603
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030149603 A1

TITLE: System and method for operating a non-linear model with missing data for use in electronic commerce

PUBLICATION-DATE: August 7, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ferguson, Bruce	Round Rock	TX	US	
Hartman, Eric	Austin	TX	US	

US-CL-CURRENT: 705/7; 706/22

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 16. Document ID: US 20030140023 A1

L2: Entry 16 of 81 File: PGPB Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030140023
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030140023 A1

TITLE: System and method for pre-processing input data to a non-linear model for use in electronic commerce

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ferguson, Bruce	Round Rock	TX	US	
Hartman, Eric	Austin	TX	US	

US-CL-CURRENT: 706/21; 706/15

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 17. Document ID: US 20030130899 A1

L2: Entry 17 of 81 File: PGPB Jul 10, 2003

PGPUB-DOCUMENT-NUMBER: 20030130899
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030130899 A1

TITLE: System and method for historical database training of non-linear models for use in electronic commerce

PUBLICATION-DATE: July 10, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ferguson, Bruce	Round Rock	TX	US	
Hartman, Eric	Austin	TX	US	

US-CL-CURRENT: 705/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 18. Document ID: US 20030105721 A1

L2: Entry 18 of 81 File: PGPB Jun 5, 2003

PGPUB-DOCUMENT-NUMBER: 20030105721
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030105721 A1

TITLE: Systems and methods for secure transaction management and electronic rights

protection

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Sunnyvale	CA	US	

US-CL-CURRENT: 705/54; 713/193

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 19. Document ID: US 20030088784 A1

L2: Entry 19 of 81 File: PGPB May 8, 2003

PGPUB-DOCUMENT-NUMBER: 20030088784
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030088784 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: May 8, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Eugene	OR	US	

US-CL-CURRENT: 713/189; 713/182, 713/194

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 20. Document ID: US 20030065939 A1

L2: Entry 20 of 81 File: PGPB Apr 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030065939
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030065939 A1

TITLE: Authentication system having a semiconductor device containing data which are difficult to analyze through illegitimate access, and semiconductor device therefor

PUBLICATION-DATE: April 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Nosaka, Takeshi	Ukyo-ku		JP	

US-CL-CURRENT: 713/200

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 21. Document ID: US 20030033587 A1

L2: Entry 21 of 81 File: PGPB Feb 13, 2003

PGPUB-DOCUMENT-NUMBER: 20030033587
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030033587 A1

TITLE: System and method for on-line training of a non-linear model for use in electronic commerce

PUBLICATION-DATE: February 13, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ferguson, Bruce	Round Rock	TX	US	
Hartman, Eric	Austin	TX	US	

US-CL-CURRENT: 717/104

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 22. Document ID: US 20020177407 A1

L2: Entry 22 of 81 File: PGPB Nov 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020177407
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020177407 A1

TITLE: Portable telephone set and IC card

PUBLICATION-DATE: November 28, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Mitsumoto, Hiroki	Kawasaki		JP	

US-CL-CURRENT: 455/41.1; 455/42, 455/513

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 23. Document ID: US 20020161688 A1

L2: Entry 23 of 81 File: PGPB Oct 31, 2002

PGPUB-DOCUMENT-NUMBER: 20020161688
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020161688 A1

TITLE: Open market collaboration system for enterprise wide electronic commerce

PUBLICATION-DATE: October 31, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Stewart, Rocky	Danville	CA	US	
Metsaportti, Timo	Espoo	CA	FI	
Takacsi-Nagy, Pal	Cupertino		US	

US-CL-CURRENT: 705/37

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 24. Document ID: US 20020128929 A1

L2: Entry 24 of 81 File: PGPB Sep 12, 2002

PGPUB-DOCUMENT-NUMBER: 20020128929
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020128929 A1

TITLE: Electronic commerce system and electronic commerce method

PUBLICATION-DATE: September 12, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Urabe, Akio	Tokyo		JP	

US-CL-CURRENT: 705/26; 705/39

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 25. Document ID: US 20020120527 A1

L2: Entry 25 of 81 File: PGPB Aug 29, 2002

PGPUB-DOCUMENT-NUMBER: 20020120527

PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020120527 A1

TITLE: Method and system for international shopping

PUBLICATION-DATE: August 29, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lam, Benson	Mississauga		CA	
Ng, Chee	Don Mills		CA	
Yu, Gregory	Toronto		CA	
Trossman, Andrew	Toronto		CA	
Yong, Teck	Vaughan		CA	
Yaphe, Katherine	Toronto		CA	
Schuurmans, Pierre	Toronto		CA	
Mihaila, Florin	Toronto		CA	
Vintila, Nicolae	North York		CA	

US-CL-CURRENT: 705/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 26. Document ID: US 20020112171 A1

L2: Entry 26 of 81 File: PGPB Aug 15, 2002

PGPUB-DOCUMENT-NUMBER: 20020112171
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020112171 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: August 15, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	
Spahn, Francis J.	El Cerrito	CA	US	
Van Wie, David M.	Eugene	OR	US	

US-CL-CURRENT: 713/185; 705/51, 713/200

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 27. Document ID: US 20020111870 A1

L2: Entry 27 of 81 File: PGPB Aug 15, 2002

PGPUB-DOCUMENT-NUMBER: 20020111870
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020111870 A1

TITLE: System and method for identifying a product

PUBLICATION-DATE: August 15, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Chinnappan, Mohanasundaram	Nashua	NH	US	
Tenorio, Manoel	Mountain View	CA	US	

US-CL-CURRENT: 705/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 28. Document ID: US 20020083048 A1

L2: Entry 28 of 81 File: PGPB Jun 27, 2002

PGPUB-DOCUMENT-NUMBER: 20020083048
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020083048 A1

TITLE: System and method for selective database indexing

PUBLICATION-DATE: June 27, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Tenorio, Manoel	Mountain View	CA	US	
Chinnappan, Mohanasundaram	Nashua	NH	US	

US-CL-CURRENT: 707/2

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 29. Document ID: US 20020082945 A1

L2: Entry 29 of 81 File: PGPB Jun 27, 2002

PGPUB-DOCUMENT-NUMBER: 20020082945
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020082945 A1

TITLE: System and method for migrating data in an electronic commerce system

PUBLICATION-DATE: June 27, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Tenorio, Manoel	Mountain View	CA	US	

US-CL-CURRENT: 705/27

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 30. Document ID: US 20020082932 A1

L2: Entry 30 of 81 File: PGPB Jun 27, 2002

PGPUB-DOCUMENT-NUMBER: 20020082932
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020082932 A1

TITLE: System and method for facilitating electronic commerce transactions

PUBLICATION-DATE: June 27, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Chinnappan, Mohanasundaram	Nashua	NH	US	
Tenorio, Manoel	Mountain View	CA	US	
Fenstermaker, Stephen	Mountain View	CA	US	
Jung, Duane F.	Palo Alto	CA	US	

US-CL-CURRENT: 705/26; 705/27

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 31. Document ID: US 20020048369 A1

L2: Entry 31 of 81 File: PGPB Apr 25, 2002

PGPUB-DOCUMENT-NUMBER: 20020048369
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020048369 A1

TITLE: Systems and methods for secure transaction management and electronic rights protection

PUBLICATION-DATE: April 25, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ginter, Karl L.	Beltsville	MD	US	
Shear, Victor H.	Bethesda	MD	US	

Sibert, W. Olin	Lexington	MA	US
Spahn, Francis J.	El Cerrito	CA	US
Van Wie, David M.	Eugene	OR	US

US-CL-CURRENT: 380/277; 380/246, 713/151, 713/194

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 32. Document ID: US 20020025044 A1

L2: Entry 32 of 81 File: PGPB Feb 28, 2002

PGPUB-DOCUMENT-NUMBER: 20020025044
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020025044 A1

TITLE: Data management system

PUBLICATION-DATE: February 28, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Saito, Makoto	Tokyo		JP	

US-CL-CURRENT: 380/278

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

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☐ 33. Document ID: US 20010051899 A1

L2: Entry 33 of 81 File: PGPB Dec 13, 2001

PGPUB-DOCUMENT-NUMBER: 20010051899
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20010051899 A1

TITLE: Document managing apparatus for managing transaction slip data in electronic commerce

PUBLICATION-DATE: December 13, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Kawashima, Takahiko	Kawasaki		JP	
Kawamura, Isamu	Kawasaki		JP	
Amaku, Hideyuki	Kobe		JP	

US-CL-CURRENT: 705/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De

☐ 34. Document ID: US 6744894 B1

L2: Entry 34 of 81 File: USPT Jun 1, 2004

US-PAT-NO: 6744894
DOCUMENT-IDENTIFIER: US 6744894 B1

TITLE: Data management system

DATE-ISSUED: June 1, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Saito; Makoto	Tokyo			JP

US-CL-CURRENT: 380/277; 705/52, 705/57, 705/59

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 35. Document ID: US 6721713 B1

L2: Entry 35 of 81 File: USPT Apr 13, 2004

US-PAT-NO: 6721713
DOCUMENT-IDENTIFIER: US 6721713 B1

TITLE: Business alliance identification in a web architecture framework

DATE-ISSUED: April 13, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guheen; Michael F.	Tiburon	CA		
Mitchell; James D.	Manhattan Beach	CA		
Barrese; James J.	San Jose	CA		

US-CL-CURRENT: 705/1; 709/223, 715/503

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 36. Document ID: US 6708161 B2

L2: Entry 36 of 81 File: USPT Mar 16, 2004

US-PAT-NO: 6708161
DOCUMENT-IDENTIFIER: US 6708161 B2

TITLE: System and method for selective database indexing

DATE-ISSUED: March 16, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Tenorio; Manoel	Mountain View	CA		
Chinnappan; Mohanasundaram	Nashua	NH		

US-CL-CURRENT: 707/2

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw Ds

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☐ 37. Document ID: US 6671818 B1

L2: Entry 37 of 81 File: USPT Dec 30, 2003

US-PAT-NO: 6671818
DOCUMENT-IDENTIFIER: US 6671818 B1
**** See image for Certificate of Correction ****

TITLE: Problem isolation through translating and filtering events into a standard object format in a network based supply chain

DATE-ISSUED: December 30, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mikurak; Michael G.	Hamilton	NJ		

US-CL-CURRENT: 714/4; 714/43, 714/48

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw Ds

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☐ 38. Document ID: US 6640304 B2

L2: Entry 38 of 81 File: USPT Oct 28, 2003

US-PAT-NO: 6640304
DOCUMENT-IDENTIFIER: US 6640304 B2
**** See image for Certificate of Correction ****

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: October 28, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		

Van Wie; David M. Eugene OR

US-CL-CURRENT: 713/193; 713/165, 713/167, 713/201

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 39. Document ID: US 6615166 B1

L2: Entry 39 of 81 File: USPT Sep 2, 2003

US-PAT-NO: 6615166
DOCUMENT-IDENTIFIER: US 6615166 B1

TITLE: Prioritizing components of a network framework required for implementation of technology

DATE-ISSUED: September 2, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guheen; Michael F.	Tiburon	CA		
Mitchell; James D.	Manhattan Beach	CA		
Barrese; James J.	San Jose	CA		

US-CL-CURRENT: 703/27; 703/26, 709/220, 709/223, 709/231, 717/140, 719/316

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 40. Document ID: US 6606744 B1

L2: Entry 40 of 81 File: USPT Aug 12, 2003

US-PAT-NO: 6606744
DOCUMENT-IDENTIFIER: US 6606744 B1
** See image for Certificate of Correction **

TITLE: Providing collaborative installation management in a network-based supply chain environment

DATE-ISSUED: August 12, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mikurak; Michael G.	Hamilton	NJ		

US-CL-CURRENT: 717/174; 705/26, 717/178

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 41. Document ID: US 6536037 B1

L2: Entry 41 of 81 File: USPT Mar 18, 2003

US-PAT-NO: 6536037
DOCUMENT-IDENTIFIER: US 6536037 B1
**** See image for Certificate of Correction ****

TITLE: Identification of redundancies and omissions among components of a web based architecture

DATE-ISSUED: March 18, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guheen; Michael F	Tiburon	CA		
Mitchell; James D.	Manhattan Beach	CA		
Barrese; James J.	San Jose	CA		

US-CL-CURRENT: 717/151; 703/2, 709/231

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 42. Document ID: US 6519571 B1

L2: Entry 42 of 81 File: USPT Feb 11, 2003

US-PAT-NO: 6519571
DOCUMENT-IDENTIFIER: US 6519571 B1
**** See image for Certificate of Correction ****

TITLE: Dynamic customer profile management

DATE-ISSUED: February 11, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guheen; Michael F.	Tiburon	CA		
Mitchell; James D.	Manhatten Beach	CA		
Barrese; James J.	San Jose	CA		

US-CL-CURRENT: 705/14

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 43. Document ID: US 6473794 B1

L2: Entry 43 of 81 File: USPT Oct 29, 2002

US-PAT-NO: 6473794
DOCUMENT-IDENTIFIER: US 6473794 B1

TITLE: System for establishing plan to test components of web based framework by displaying pictorial representation and conveying indicia coded components of existing network framework

DATE-ISSUED: October 29, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guheen; Michael F.	Tiburon	CA		
Mitchell; James D.	Manhattan Beach	CA		
Barrese; James J.	San Jose	CA		

US-CL-CURRENT: 709/223; 709/224

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 44. Document ID: US 6427140 B1

L2: Entry 44 of 81 File: USPT Jul 30, 2002

US-PAT-NO: 6427140
DOCUMENT-IDENTIFIER: US 6427140 B1
**** See image for Certificate of Correction ****

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: July 30, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethseda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Sunnyvale	CA		

US-CL-CURRENT: 705/80; 705/53, 713/193

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 45. Document ID: US 6389402 B1

L2: Entry 45 of 81 File: USPT May 14, 2002

US-PAT-NO: 6389402
DOCUMENT-IDENTIFIER: US 6389402 B1
**** See image for Certificate of Correction ****

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: May 14, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Eugene	OR		

US-CL-CURRENT: 705/51; 380/201, 705/1, 705/37, 705/53, 705/57, 705/80

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 46. Document ID: US 6363488 B1

L2: Entry 46 of 81 File: USPT Mar 26, 2002

US-PAT-NO: 6363488
DOCUMENT-IDENTIFIER: US 6363488 B1
**** See image for Certificate of Correction ****

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: March 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Eugene	OR		

US-CL-CURRENT: 713/201; 705/14, 705/53

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 47. Document ID: US 6253193 B1

L2: Entry 47 of 81 File: USPT Jun 26, 2001

US-PAT-NO: 6253193
DOCUMENT-IDENTIFIER: US 6253193 B1
**** See image for Certificate of Correction ****

TITLE: Systems and methods for the secure transaction management and electronic rights protection

DATE-ISSUED: June 26, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Sunnyvale	CA		

US-CL-CURRENT: 705/57; 705/52

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 48. Document ID: US 6237786 B1

L2: Entry 48 of 81 File: USPT May 29, 2001

US-PAT-NO: 6237786
DOCUMENT-IDENTIFIER: US 6237786 B1

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: May 29, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Eugene	OR		

US-CL-CURRENT: 213/153; 380/203, 705/51, 705/58

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 49. Document ID: US 5982891 A

L2: Entry 49 of 81 File: USPT Nov 9, 1999

US-PAT-NO: 5982891
DOCUMENT-IDENTIFIER: US 5982891 A

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: November 9, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		

Spahn; Francis J.	El Cerrito	CA
Van Wie; David M.	Sunnyvale	CA

US-CL-CURRENT: 705/54; 705/26, 713/167

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D

☐ 50. Document ID: US 5974141 A

L2: Entry 50 of 81 File: USPT Oct 26, 1999

US-PAT-NO: 5974141
DOCUMENT-IDENTIFIER: US 5974141 A

TITLE: Data management system

DATE-ISSUED: October 26, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Saito; Makoto	Tokyo			JP

US-CL-CURRENT: 705/52; 705/57

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
L1 and electronic near commerce near data	81

Display Format: - Change Format

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E
N
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Hit List

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Search Results - Record(s) 51 through 81 of 81 returned.

☐ 51. Document ID: US 5949876 A

Using default format because multiple data bases are involved.

L2: Entry 51 of 81 File: USPT Sep 7, 1999

US-PAT-NO: 5949876
DOCUMENT-IDENTIFIER: US 5949876 A
**** See image for Certificate of Correction ****

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: September 7, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Sunnyvale	CA		

US-CL-CURRENT: 705/80; 705/1, 705/39, 705/54

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

☐ 52. Document ID: US 5917912 A

L2: Entry 52 of 81 File: USPT Jun 29, 1999

US-PAT-NO: 5917912
DOCUMENT-IDENTIFIER: US 5917912 A
**** See image for Certificate of Correction ****

TITLE: System and methods for secure transaction management and electronic rights protection

DATE-ISSUED: June 29, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		

Spahn; Francis J.	El Cerrito	CA
Van Wie; David M.	Sunnyvale	CA

US-CL-CURRENT: 713/187; 705/40, 713/164, 719/312

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 53. Document ID: US 5915019 A

L2: Entry 53 of 81 File: USPT Jun 22, 1999

US-PAT-NO: 5915019
DOCUMENT-IDENTIFIER: US 5915019 A

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: June 22, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Sunnyvale	CA		

US-CL-CURRENT: 705/54; 705/26, 705/400, 713/200

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 54. Document ID: US 5910987 A

L2: Entry 54 of 81 File: USPT Jun 8, 1999

US-PAT-NO: 5910987
DOCUMENT-IDENTIFIER: US 5910987 A

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: June 8, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Sunnyvale	CA		

US-CL-CURRENT: 705/52; 705/30

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 55. Document ID: US 5903652 A

L2: Entry 55 of 81 File: USPT May 11, 1999

US-PAT-NO: 5903652
DOCUMENT-IDENTIFIER: US 5903652 A
** See image for Certificate of Correction **

TITLE: System and apparatus for monitoring secure information in a computer network

DATE-ISSUED: May 11, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mital; Amit	Kirkland	WA		

US-CL-CURRENT: 705/78; 705/26, 705/75

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 56. Document ID: US 5892900 A

L2: Entry 56 of 81 File: USPT Apr 6, 1999

US-PAT-NO: 5892900
DOCUMENT-IDENTIFIER: US 5892900 A
** See image for Certificate of Correction **

TITLE: Systems and methods for secure transaction management and electronic rights protection

DATE-ISSUED: April 6, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginter; Karl L.	Beltsville	MD		
Shear; Victor H.	Bethesda	MD		
Sibert; W. Olin	Lexington	MA		
Spahn; Francis J.	El Cerrito	CA		
Van Wie; David M.	Sunnyvale	CA		

US-CL-CURRENT: 713/200; 713/201

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 57. Document ID: US 5794234 A

L2: Entry 57 of 81 File: USPT Aug 11, 1998

US-PAT-NO: 5794234
DOCUMENT-IDENTIFIER: US 5794234 A

TITLE: Method and system for providing electronic commerce between incompatible data processing systems

DATE-ISSUED: August 11, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Church; Craig A	Palo Alto	CA		
Chaban; Joel	San Rafael	CA		
Erbaugh; Mark	Grove City	OH		

US-CL-CURRENT: 707/4; 705/16, 705/30, 705/35, 707/10, 709/201, 709/213

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 58. Document ID: JP 2004021829 A

L2: Entry 58 of 81 File: JPAB Jan 22, 2004

PUB-NO: JP02004021829A
DOCUMENT-IDENTIFIER: JP 2004021829 A
TITLE: ELECTRONIC COMMERCE DATA MANAGEMENT SYSTEM

PUBN-DATE: January 22, 2004

INVENTOR-INFORMATION:

NAME	COUNTRY
NAGAI, TOKUHITO	

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 59. Document ID: JP 2002259585 A

L2: Entry 59 of 81 File: JPAB Sep 13, 2002

PUB-NO: JP02002259585A
DOCUMENT-IDENTIFIER: JP 2002259585 A
TITLE: SYSTEM, METHOD, AND PROGRAM FOR IMPLEMENTING ELECTRONIC TRADE USING ASP SERVICE PROVIDING SYSTEM, AND COMPUTER READABLE RECORDING MEDIUM

PUBN-DATE: September 13, 2002

INVENTOR-INFORMATION:

NAME	COUNTRY
IWAMOTO, RETSUBU	
KUWANA, KENJI	

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 60. Document ID: JP 2002197218 A

L2: Entry 60 of 81 File: JPAB Jul 12, 2002

PUB-NO: JP02002197218A
DOCUMENT-IDENTIFIER: JP 2002197218 A
TITLE: SYSTEM FOR PROVIDING CERTIFICATION THAT ELECTRONIC INFORMATION HAS BEEN PUBLICLY LAID OPEN IN SYSTEM FOR CERTIFICATION THAT ELECTRONIC INFORMATION HAS BEEN PUBLICLY LAID OPEN, SYSTEM FOR PROVIDING ACCESS CERTIFICATION DATA, ELECTRONIC COMMERCE SYSTEM AND METHOD AND PROGRAM FOR THE SYSTEM AND COMPUTER- READABLE STORAGE MEDIUM IN WHICH THE PROGRAM IS STORED.

PUBN-DATE: July 12, 2002

INVENTOR-INFORMATION:

NAME	COUNTRY
KANAI, YOICHI	

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 61. Document ID: WO 2004008281 A2

L2: Entry 61 of 81 File: EPAB Jan 22, 2004

PUB-NO: WO2004008281A2
DOCUMENT-IDENTIFIER: WO 2004008281 A2
TITLE: INTERACTIVE ELECTRONIC COMMERCE AND MESSAGE INTERCHANGE SYSTEM FEATURING DELIVERY OF MESSAGES TAILORED TO INDIVIDUAL USERS

PUBN-DATE: January 22, 2004

INVENTOR-INFORMATION:

NAME	COUNTRY
NEMIROFSKY, FRANK ROBERT	
LINCOLN, LARRY A	

INT-CL (IPC): G06 F 0/

EUR-CL (EPC): G06F017/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 62. Document ID: JP 2004318452 A

L2: Entry 62 of 81 File: DWPI Nov 11, 2004

DERWENT-ACC-NO: 2004-779772
DERWENT-WEEK: 200477
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Data access limiting system in electronic commerce, determines access right of requester to business data based on access limit data and corporate information provided by access provider and requester respectively

PRIORITY-DATA: 2003JP-0110994 (April 16, 2003)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2004318452 A	November 11, 2004		008	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 63. Document ID: KR 2004020837 A

L2: Entry 63 of 81 File: DWPI Mar 9, 2004

DERWENT-ACC-NO: 2004-474981
DERWENT-WEEK: 200445
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: System and method for certifying delivery man

INVENTOR: KIM, Y S

PRIORITY-DATA: 2003KR-0044720 (June 28, 2003), 2002KR-0053342 (September 2, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2004020837 A	March 9, 2004		001	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 64. Document ID: US 6744894 B1

L2: Entry 64 of 81 File: DWPI Jun 1, 2004

DERWENT-ACC-NO: 2004-466660
DERWENT-WEEK: 200444
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Digital data management system for copyright protected data, includes data management center to transfer edit label to subsequent user, except digital data which is not stored in device of user

INVENTOR: SAITO, M

PRIORITY-DATA: 1994JP-0269959 (November 2, 1994), 1994JP-0064889 (April 1, 1994), 1994JP-0237673 (September 30, 1994), 1994JP-0264201 (October 27, 1994)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
US 6744894 B1	June 1, 2004		026	H04L009/00

INT-CL (IPC): H04 L 9/00

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 65. Document ID: JP 2004110487 A

L2: Entry 65 of 81 File: DWPI Apr 8, 2004

DERWENT-ACC-NO: 2004-322461
DERWENT-WEEK: 200430
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Electronic banking authentication system for electronic commerce, has credit card payment system that authenticates secrecy information regarding electronic banking contained in electronic commerce data

PRIORITY-DATA: 2002JP-0272982 (September 19, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2004110487 A	April 8, 2004		011	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 66. Document ID: JP 2004021829 A

L2: Entry 66 of 81 File: DWPI Jan 22, 2004

DERWENT-ACC-NO: 2004-137829
DERWENT-WEEK: 200414
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Electronic commerce data management system stores continuously electronic commerce data received from user terminal, in data storage portion after completion of electronic commerce

PRIORITY-DATA: 2002JP-0178850 (June 19, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2004021829 A	January 22, 2004		008	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 67. Document ID: JP 2003296618 A

L2: Entry 67 of 81 File: DWPI Oct 17, 2003

DERWENT-ACC-NO: 2003-795884
DERWENT-WEEK: 200375
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TITLE: Electronic commerce method for companies, involves notifying orderer of non-acquired electronic commerce data list, through electronic mail

PRIORITY-DATA: 2002JP-0102712 (April 4, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2003296618 A	October 17, 2003		015	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

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☐ 68. Document ID: AU 2003205206 A1, WO 2003062952 A2, US 20030149603 A1

L2: Entry 68 of 81 File: DWPI Sep 2, 2003

DERWENT-ACC-NO: 2003-577933
DERWENT-WEEK: 200422
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TITLE: Nonlinear model with missing data replacement operating system in electronic commerce to correct for missing or bad data and/or for time delay in network

INVENTOR: FERGUSON, B; HARTMAN, E

PRIORITY-DATA: 2002US-0051598 (January 18, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
AU 2003205206 A1	September 2, 2003		000	G06F000/00
WO 2003062952 A2	July 31, 2003	E	054	G06F000/00
US 20030149603 A1	August 7, 2003		000	G06F017/60

INT-CL (IPC): G06 F 0/00; G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 69. Document ID: AU 2003205205 A1, US 20030140023 A1, WO 2003063015 A1

L2: Entry 69 of 81 File: DWPI Sep 2, 2003

DERWENT-ACC-NO: 2003-710175
DERWENT-WEEK: 200422
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TITLE: Data preprocessor for electronic-commerce system, has time merge device to select predetermined time scale and to reconcile input e-commerce data corresponding to selected time scale

INVENTOR: FERGUSON, B; HARTMAN, E

PRIORITY-DATA: 2002US-0051421 (January 18, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
AU 2003205205 A1	September 2, 2003		000	G06F015/18
US 20030140023 A1	July 24, 2003		088	G06E001/00
WO 2003063015 A1	July 31, 2003	E	000	G06F015/18

INT-CL (IPC): G06 E 1/00; G06 F 15/18

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 70. Document ID: AU 2003217177 A1, US 20030130899 A1, WO 2003060822 A1

L2: Entry 70 of 81 File: DWPI Jul 30, 2003

DERWENT-ACC-NO: 2003-709458
DERWENT-WEEK: 200421
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Non-linear model training method for electronic commerce systems, involves selecting electronic commerce training input data time period based on input data timestamps to retrieve electronic commerce input data

INVENTOR: FERGUSON, B; HARTMAN, E

PRIORITY-DATA: 2002US-0041403 (January 8, 2002)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
AU 2003217177 A1	July 30, 2003		000	G06N005/00
US 20030130899 A1	July 10, 2003		100	G06F017/60
WO 2003060822 A1	July 24, 2003	E	000	G06N005/00

INT-CL (IPC): G06 F 17/60; G06 N 5/00

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 71. Document ID: KR 2003037770 A

L2: Entry 71 of 81 File: DWPI May 16, 2003

DERWENT-ACC-NO: 2003-624263
DERWENT-WEEK: 200359
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Method for synchronizing data in electronic commerce system

INVENTOR: LEE, H I

PRIORITY-DATA: 2001KR-0068629 (November 5, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2003037770 A	May 16, 2003		001	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 72. Document ID: JP 2003132293 A

L2: Entry 72 of 81 File: DWPI May 9, 2003

DERWENT-ACC-NO: 2003-425219
DERWENT-WEEK: 200340
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Fund transfer system for electronic commerce, judges whether amount of fund transferred to payment terminal is appropriate based on bank transfer data, using which funds is transferred to allocation terminal of seller

PRIORITY-DATA: 2001JP-0322093 (October 19, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2003132293 A	May 9, 2003		008	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 73. Document ID: JP 2003099644 A

L2: Entry 73 of 81 File: DWPI Apr 4, 2003

DERWENT-ACC-NO: 2003-347946
DERWENT-WEEK: 200333

COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Goods ordering system for electronic commerce, has data generator which extracts kit goods information, and generates order data with respect to each article indicated in kit goods information

PRIORITY-DATA: 2001JP-0288613 (September 21, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2003099644 A	April 4, 2003		009	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 74. Document ID: KR 2003001722 A

L2: Entry 74 of 81 File: DWPI Jan 8, 2003

DERWENT-ACC-NO: 2003-350823
DERWENT-WEEK: 200333
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Payment system and method using automatic classification of payment data in electronic commerce system

INVENTOR: KIM, Y; KWON, J H ; SIM, S H

PRIORITY-DATA: 2001KR-0037065 (June 27, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2003001722 A	January 8, 2003		001	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 75. Document ID: JP 2002358398 A

L2: Entry 75 of 81 File: DWPI Dec 13, 2002

DERWENT-ACC-NO: 2003-204328
DERWENT-WEEK: 200320
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Consumption trend analysis system in electronic commerce has data processing unit which generates order situation data for each group of customers as order data with reference to respective database

PRIORITY-DATA: 2001JP-0167566 (June 4, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2002358398 A	December 13, 2002		012	G06F017/60

INT-CL (IPC): G06 F 17/30; G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 76. Document ID: JP 2002334274 A

L2: Entry 76 of 81 File: DWPI Nov 22, 2002

DERWENT-ACC-NO: 2003-072106
DERWENT-WEEK: 200307
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Profile management system for electronic commerce, has data directories which store users certification information, private information and service information, provided in profile management server

PRIORITY-DATA: 2001JP-0136871 (May 8, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2002334274 A	November 22, 2002		007	G06F017/60

INT-CL (IPC): G06 F 12/14; G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 77. Document ID: DE 10196670 T, WO 200227557 A1, US 20020082945 A1, AU 200196319 A

L2: Entry 77 of 81 File: DWPI Aug 21, 2003

DERWENT-ACC-NO: 2002-643199
DERWENT-WEEK: 200362
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Product data migrating for e-commerce, monitors requests using directory structure, product pointers, defined attributes and database to initiate migration

INVENTOR: TENORIO, M

PRIORITY-DATA: 2000US-0745374 (December 22, 2000), 2000US-235945P (September 26, 2000)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
DE 10196670 T	August 21, 2003		000	G06F017/30
WO 200227557 A1	April 4, 2002	E	017	G06F017/30
US 20020082945 A1	June 27, 2002		000	G06F017/60
AU 200196319 A	April 8, 2002		000	G06F017/30

INT-CL (IPC): G06 F 15/173; G06 F 17/30; G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 78. Document ID: EP 1191739 A2

L2: Entry 78 of 81 File: DWPI Mar 27, 2002

DERWENT-ACC-NO: 2002-342413
DERWENT-WEEK: 200238
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Data encryption-decryption system for electronic commerce, has encryption accelerator to execute encryption algorithm without using central processing unit resources

INVENTOR: DUVAL, D E

PRIORITY-DATA: 2001US-0916557 (July 26, 2001), 2000US-235190P (September 25, 2000)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
EP 1191739 A2	March 27, 2002	E	015	H04L009/18

INT-CL (IPC): H04 L 9/18

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 79. Document ID: KR 2001044449 A

L2: Entry 79 of 81 File: DWPI Jun 5, 2001

DERWENT-ACC-NO: 2001-645602
DERWENT-WEEK: 200174
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Delivery system in connection with electronic commerce logistics based on xml

INVENTOR: LEE, H

PRIORITY-DATA: 2001KR-0008816 (February 21, 2001)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
KR 2001044449 A	June 5, 2001		001	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

☐ 80. Document ID: CN 1399755 A, WO 200139057 A1, AU 200118986 A, KR

2001051103 A, KR 2001051457 A, KR 2001051710 A, EP 1259913 A1, KR 2002059784 A, JP 2003515820 W

L2: Entry 80 of 81 File: DWPI Feb 26, 2003

DERWENT-ACC-NO: 2001-356078
DERWENT-WEEK: 200337
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Electronic commerce system using a bank account and an accounting limit of credit for an electronic credit card over the Internet for safe transactions

INVENTOR: LEE, S W

PRIORITY-DATA: 2000KR-0014646 (March 22, 2000), 1999KR-0052538 (November 24, 1999)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
CN 1399755 A	February 26, 2003		000	G06F017/60
WO 200139057 A1	May 31, 2001	E	052	G06F017/60
AU 200118986 A	June 4, 2001		000	G06F017/60
KR 2001051103 A	June 25, 2001		000	G06F017/60
KR 2001051457 A	June 25, 2001		000	G06F017/60
KR 2001051710 A	June 25, 2001		000	G06F017/60
EP 1259913 A1	November 27, 2002	E	000	G06F017/60
KR 2002059784 A	July 13, 2002		000	G06F017/60
JP 2003515820 W	May 7, 2003		057	G06F017/60

INT-CL (IPC): G06 F 17/60

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D

☐ 81. Document ID: JP 2004343782 A, EP 833241 A2, JP 10107787 A

L2: Entry 81 of 81 File: DWPI Dec 2, 2004

DERWENT-ACC-NO: 1998-181388
DERWENT-WEEK: 200479
COPYRIGHT 2005 DERWENT INFORMATION LTD
TITLE: Secure data management system for ensuring security of data in computer network - provides secret-, public- and private-keys, applies data-, data owner- and data user-labels, with headers, confirming validity before decryption and transfer of data

INVENTOR: SAITO, M

PRIORITY-DATA: 1996JP-0277125 (September 27, 1996), 2004JP-0168776 (June 7, 2004)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 2004343782 A	December 2, 2004		035	H04L009/08
EP 833241 A2	April 1, 1998	E	033	G06F001/00

JP 10107787 A April 24, 1998 022 H04L009/08

INT-CL (IPC): G06 F 1/00; G06 F 12/14; G06 F 15/00; G09 C 1/00; H04 L 9/08; H04 L 9/14; H04 L 9/32

Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De

Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS

Terms	Documents
L1 and electronic near commerce near data	81

Display Format: - Change Format

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File 347:JAPIO Nov 1976-2004/Sep(Updated 050204)
         (c) 2005 JPO & JAPIO
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200509
         (c) 2005  Thomson Derwent

Set     Items   Description
S1      15023   (NONLINEAR OR NON()LINEAR)(1W)(MODEL? ? OR SYSTEM? ?) OR N-
             EURAL?()(NET? ? OR NETWORK? ?) OR AI OR ARTIFICIAL()INTELLIGE-
             NCE
S2     336371   TIMESCALE? ? OR TIMEBAR? ? OR TIMELINE? ? OR TIME(3N)(SCAL-
             E? ? OR UNIT? ? OR PERIOD? ? OR INTERVAL? ? OR MEASURE? ? OR -
             BAR? ? OR LINE? ? OR SERIES)
S3      20166   (COMMON OR SINGLE OR SINGULAR OR ONE OR MASTER OR TEMPLATE
             OR SAME OR UNIFORM OR CONSISTENT)(5N)S2
S4        884   S3(20N)(CONFORM? OR RECONCIL? OR ADAPT? OR CONVERT??? OR C-
             ONVERSION OR TRANSLAT? OR TRANSFORM? OR MAP????)
S5       1512   S3(20N)(MERG??? OR COMBIN? OR FUSE? ? OR FUSING OR CHANG???
              OR ADJUST??? OR ADJUSTMENT OR MODIF???? OR MODIFICATION OR A-
             LTER??? OR ALTERATION)
S6      18127   (TRAINING OR "TEST")(1W)(PATTERN? ? OR DATA OR STRING? ?)
S7          5   S1 AND S4
S8          6   S1 AND S5
S9          2   S6 AND S4
S10         6   S6 AND S5
S11        16   S7:S10
S12       850   (TRAINING OR "TEST")(1W)VECTOR? ?
S13         1   S12 AND S4:S5
S14         0   S13 NOT S11
```

11/5/1 (Item 1 from file: 347)
DIALOG(R)File 347:JAPIO

08158520 **Image available**
DIGITAL EQUIPMENT INSPECTION DEVICE AND DIGITAL EQUIPMENT INSPECTION METHOD

PUB. NO.: 2004-271280 [JP 2004271280 A]
PUBLISHED: September 30, 2004 (20040930)
INVENTOR(s): CHINO KAZUHITO
APPLICANT(s): SEIKO EPSON CORP
APPL. NO.: 2003-060264 [JP 200360264]
FILED: March 06, 2003 (20030306)
INTL CLASS: G01R-031/319; G01R-031/3183; G02F-001/13; G02F-001/133

ABSTRACT

PROBLEM TO BE SOLVED: To provide an inspection device and an inspection method for automatically inspecting a plurality of output terminals (interfaces) of digital equipment (circuits), by preventing abnormal check mistakes by a difference in **test patterns** , or the like, and without overlooking **changes** in data for a specified **period** of **time** .

SOLUTION: Data, where only **one** terminal in output terminals becomes "1" and all remaining terminals go to "0", if a video circuit 100b operates normally, are generated for a specific period and are inputted to the video circuit 100b, namely digital equipment having a specific number (N) of output terminals by a signal generation means 100a. Further, terminals that go to "1" for each specific period passage are changed successively, and the data are inputted to all specific number (N) of the terminals. An inspection section 200 monitors the output of each output terminal in the video circuit 100b for a specified period and compares an N-bit value after monitoring with a normal N-bit value for deciding the quality of the video circuit 100b.

11/5/3 (Item 3 from file: 347)
DIALOG(R)File 347:JAPIO

06134915 **Image available**
MULTIPLE INPUT TYPE REHABILITATION SUPPORTING MACHINE

PUB. NO.: 11-076455 [JP 11076455 A]
PUBLISHED: March 23, 1999 (19990323)
INVENTOR(s): OHASHI MAYUMI
APPLICANT(s): ARUMENI KK
APPL. NO.: 09-279299 [JP 97279299]
FILED: September 04, 1997 (19970904)
INTL CLASS: A63B-023/035; G09B-019/00; G10K-015/04

ABSTRACT

PROBLEM TO BE SOLVED: To perform effective rehabilitation and facilitate a grasp of a training result by using a sound generator for setting of training contents and an input instruction as a main body and providing musical element constructed of a plurality of input switch units, to which input can be carried out by means of a finger tip, an elbow, or a foot, separately from a main body.

SOLUTION: When used, a power source switch is turned on, and a **training pattern** and a training mode are set on a main body side. Selection of a music, sound tone setting, sound volume setting, vibration instruction setting, a result printing setting are carried out, and at the **same time** , a musical **scale** or an octave **converting** function is set on the switch unit side. Then, a start, switch is pushed for starting training, and in

this process, a trainee waits for an instruction, which is given in the form of light or vibration generated from the switch unit, for pushing a switch while independently separated input switches are placed on a table, held in the hand, or placed on the floor, and then, operates the instructed switch by means of a finer, a hand, an elbow, or a foot so as to train a target body part.

11/5/4 (Item 4 from file: 347)
DIALOG(R)File 347:JAPIO

05476480 **Image available**
DOCUMENT PREPARATION DEVICE WITH COOCURRENCE INFORMATION UTILIZATION FUNCTION AND UTILIZING METHOD FOR COOCURRENCE INFORMATION OF DOCUMENT PREPARATION DEVICE

PUB. NO.: 09-091280 [JP 9091280 A]
PUBLISHED: April 04, 1997 (19970404)
INVENTOR(s): TAKAHASHI MIRAI
APPLICANT(s): TOSHIBA CORP [000307] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 07-246433 [JP 95246433]
FILED: September 25, 1995 (19950925)
INTL CLASS: [6] G06F-017/22
JAPIO CLASS: 45.4 (INFORMATION PROCESSING -- Computer Applications)
JAPIO KEYWORD:R139 (INFORMATION PROCESSING -- Word Processors)

ABSTRACT

PROBLEM TO BE SOLVED: To generate a semantically continuous document based on the **same** coocurrence information even at the **time** of dividing a **series** of reading corresponding to coocurrence information to plural reading ranges to perform **conversion** .

SOLUTION: When KANA(Japaense syllabary) characters 'mikanno' are inputted from a key input part 12 and KANJI(Chinese character) conversion is indicated, a KANA-KANJI dictionary part 15 is referred to retrieve homophonic KANJI entries 'mikanno(of orange)' and 'mikanno(incomplete)', and an **AI** dictionary part 17 is referred through a coocurrence reference part 16 to read out coocurrence information 'mikano+no+kawa(peel of orange)', 'mikano+no+hana(flower of orange)', 'mikan+no+sakuhin(incomplete work)', 'mikan+no+ shousetsi(incomplete novel)', and 'mikano+no+taiki(incomplete great talent)', and they are stored in an output information storage part 18. When KANA characters 'taiki' are inputted next and KANJI conversion is indicated, homophonetic entry words 'taiki(wait)', 'taiki(air)', and 'taiki(great talent)' are retrieved, and coocurrence information for each homophonetic KANJI entry in the preceding conversion which is stored in the output information storage part 18 is referred to retrieve the coocurrence relation 'mikan+no+taiki(incomplete great talent)' between entries, thus prefentially displaying 'mikan(incomplete)' and 'taiki(great talent)'.

11/5/5 (Item 5 from file: 347)
DIALOG(R)File 347:JAPIO

03563786 **Image available**
SEMICONDUCTOR INTEGRATED CIRCUIT

PUB. NO.: 03-226686 [JP 3226686 A]
PUBLISHED: October 07, 1991 (19911007)
INVENTOR(s): SUZUKI EIJI
APPLICANT(s): NEC CORP [000423] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 02-022638 [JP 9022638]

FILED: January 31, 1990 (19900131)
INTL CLASS: [5] G01R-031/28; H01L-027/04
JAPIO CLASS: 46.1 (INSTRUMENTATION -- Measurement); 42.2 (ELECTRONICS -- Solid State Components)
JOURNAL: Section: P, Section No. 1295, Vol. 16, No. 5, Pg. 16, January 08,. 1992 (19920108)

ABSTRACT

PURPOSE: To efficiently measure both levels of the outputs of respective buffers at the same time by internally providing a circuit fixing the outputs of the respective buffers to an H or L level and a circuit forcibly reversing the buffers.

CONSTITUTION: When a control terminal 21 and a reversal terminal 23 are within an L-level range, a circuit shows usual operation but, when the terminal 21 becomes an H-level, a three-solid state output buffer 31 and an I/O buffer 32 are fixed to a usual output mode. Since the terminal 22 is held to an L-level in this state, the H-level outputs of a normal output buffer 30 and both buffers 31, 3 can be measured. Next, when the terminal becomes an H-level, the outputs of the respective buffers 30 - 32 are reversed and the L-level outputs of the buffers can be measured. As mentioned above, the output H- and L-levels of the arbitrary buffers 30 - 32 can be efficiently **measured** at the **same time** by **changing** over the terminals 21, 22 without using a large number of complicated **test patterns** and a test time can be shortened and the number of processes can be reduced.

11/5/8 (Item 3 from file: 350)

014966063 **Image available**
WPI Acc No: 2003-026577/200302
Apparatus and method for certifying function of system using estimation board in multichannel environment
Patent Assignee: HYNIX SEMICONDUCTOR INC (HYNI-N)
Inventor: YANG H Y
Number of Countries: 001 Number of Patents: 002
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
KR 2002052515	A	20020704	KR 200081812	A	20001226	200302	B
KR 364006	B	20021211	KR 200081812	A	20001226	200335	

Priority Applications (No Type Date): KR 200081812 A 20001226
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
KR 2002052515	A		1	H04B-017/00	
KR 364006	B			H04B-017/00	Previous Publ. patent KR 2002052515

Abstract (Basic): KR 2002052515 A

NOVELTY - An apparatus and method for certifying functions of a system using an estimation board in multichannel environment is provided to construct test environment for certifying channel-classified system functions using the estimation board in software and simultaneously certify the channel-classified system functions according to a corresponding channel-classified test mode in the constructed test environment.

DETAILED DESCRIPTION - A system function certifying unit(11) sets and **changes** a channel-classified test mode at a **uniform time interval** , and simultaneously certifies a vocoding function, a bypass function, and a channel-classified system function of a data service function using **test data** according to the changed channel-classified test mode. An SIU(Serial Interface Unit)(12) performs a serial interface with the system function certifying unit(11) and a plurality of codecs 1-N(13-1 to 13-N). A plurality of codecs 1-N(13-1 to 13-N) encode PCM signals inputted through a microphone as packet signals, and decode the packet signals inputted

through the SIU(12) as the PCM signals.
pp; 1 DwgNo 1/10
Title Terms: APPARATUS; METHOD; CERTIFY; FUNCTION; SYSTEM; ESTIMATE; BOARD; MULTICHANNEL; ENVIRONMENT
Derwent Class: W02
International Patent Class (Main): H04B-017/00
File Segment: EPI

11/5/9 (Item 4 from file: 350)
DIALOG(R)File 350:Derwent WPIX

012661521 **Image available**
WPI Acc No: 1999-467626/199939
XRPX Acc No: N99-349067
Training method for neural network **used in elevator dispatch system**
Patent Assignee: OTIS ELEVATOR CO (OTIS)
Inventor: CHRISTY T M; POWELL B A; WHITEHALL B L
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
US 5923004	A	19990713	US 97748	A	19971230	199939	B

Priority Applications (No Type Date): US 97748 A 19971230
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 5923004	A		8	B66B-001/18	

Abstract (Basic): US 5923004 A
NOVELTY - The method involves the steps of scaling the inputs to the **neural network** so that the inputs fall within a predetermined input range, determining whether an observed RRT, corresponding to an estimated RRT and so **measured** from the **same time** , exceeds a maximal allowable RRT value and **adjusting** the weights of the network using a learning rule suitable for the network architecture.
USE - The method is designed to be used to train a **neural network** to calculate an estimated remaining response time (RRT) for an elevator car to serve a hall call.
ADVANTAGE - The network is continually trained during use of the elevator due to the learning rule accounting for how the observed RRT differs from the corresponding estimated RRT. The method accounts for intervening calls, by recalculating the estimated RRT each time the elevator stops.
DESCRIPTION OF DRAWING(S) - The drawing shows a process diagram showing the method of training the **neural network** .
pp; 8 DwgNo 3/4
Title Terms: TRAINING; METHOD; NEURAL; NETWORK; ELEVATOR; DISPATCH; SYSTEM
Derwent Class: Q38; T01; T06; X25
International Patent Class (Main): B66B-001/18
File Segment: EPI; EngPI

11/5/10 (Item 5 from file: 350)
DIALOG(R)File 350:Derwent WPIX

011234511 **Image available**
WPI Acc No: 1997-212414/199719
XRPX Acc No: N97-175282
Test pattern **vector re-use for single integrated circuit logic core under test - converting test vector into two vectors, loading test register with first vector, passing signal in test register to core concurrently with applying second vector in second time period, and repeating until core is tested with each vector**
Patent Assignee: ADVANCED MICRO DEVICES INC (ADMI)
Inventor: NAIR H B; TUPURI R S
Number of Countries: 001 Number of Patents: 001

Patent Family:
Patent No Kind Date Applicat No Kind Date Week
US 5617431 A 19970401 US 94284163 A 19940802 199719 B

Priority Applications (No Type Date): US 94284163 A 19940802
Patent Details:
Patent No Kind Lan Pg Main IPC Filing Notes
US 5617431 A 13 G06F-011/00

Abstract (Basic): US 5617431 A

Test vectors are applied to a single integrated circuit containing a logic core for which a pre-existing test vector set exists. Each test vector ordinarily applied in one cycle to test a core by itself, is **converted** into a first and second test vector. The first test vector is applied to input pins of the **single** integrated circuit during a first **time period** .

Test registers connected to the input pins of the integrated circuit are loaded with signal values from the first test vector. The test registers are loaded according to a load signal. The test registers are connected between the input pins and a first set of drivers, the drivers being connected to the logic core under test. The second test vector is applied through the input pins to a second set of drivers during a second time period. A test mode signal is provided from a test interface to control the drivers. The signals stored in the test registers are provided concurrently with the signals applied to the input pins of the integrated circuit during the second time period to the logic core under test through the first and second drivers respectively.

ADVANTAGE - Reduces test generation time for circuit having more than one core. Avoids developing completely new set of test vectors.

Dwg.5/6

Title Terms: TEST; PATTERN; VECTOR; SINGLE; INTEGRATE; CIRCUIT; LOGIC; CORE ; TEST; CONVERT; TEST; VECTOR; TWO; VECTOR; LOAD; TEST; REGISTER; FIRST; VECTOR; PASS; SIGNAL; TEST; REGISTER; CORE; CONCURRENT; APPLY; SECOND; VECTOR; SECOND; TIME; PERIOD; REPEAT; CORE; TEST; VECTOR
Derwent Class: S01; T01; U11
International Patent Class (Main): G06F-011/00
File Segment: EPI

11/5/11 (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX

010590476 **Image available**
WPI Acc No: 1996-087429/199609
XRPX Acc No: N96-073367

Neural network **based data fusing method - transforming information at set** time interval **in to geographical grids with** one **being formed for each sensor reading,** combining **to form consolidated representations and analysing to out estimate of contact where observation was made**

Patent Assignee: US SEC OF NAVY (USNA); US DEPT OF THE NAVY (USNA)
Inventor: DEANGELIS C M; GREEN R W; DEANGELIS C; GREEN R
Number of Countries: 001 Number of Patents: 002
Patent Family:
Patent No Kind Date Applicat No Kind Date Week
US N8324641 N 19951115 US 94324641 A 19941018 199609 B
US 5537511 A 19960716 US 94324641 A 19941018 199634

Priority Applications (No Type Date): US 94324641 A 19941018
Patent Details:
Patent No Kind Lan Pg Main IPC Filing Notes
US N8324641 N 31 G06F-000/00
US 5537511 A 10 G06E-001/00

Abstract (Basic): US N8324641 N

The method involves estimating the state of the contact using information about a location of an observer platform e.g. ship, land observer at particular time intervals and from a sensor e.g. sonar,

radar, binoculars, telescopes about a position of the moving contact e.g. bearing angle, angle of arrival measurement relative to the observer platform at each interval. The input information is transformed in to a series of geographical grids with one being formed for each reading of one sensor using a grid stimulation unit.

A fusion unit combines the grids corresponding to similar time intervals into a series of consolidated grid representations. The consolidated grid representations are analysed to produce a desired estimate of the state of contact at a final point in time where an observation was made by using a estimation unit. A correlation unit provides a path likelihood vector.

ADVANTAGE - Increases execution speed, assessment of solution sensitivity and sensor fusion.

Dwg.4/7

US 8324641 N

The method involves estimating the state of the contact using information about a location of an observer platform e.g. ship, land observer at particular time intervals and from a sensor e.g. sonar, radar, binoculars, telescopes about a position of the moving contact e.g. bearing angle, angle of arrival measurement relative to the observer platform at each interval. The input information is transformed in to a series of geographical grids with one being formed for each reading of one sensor using a grid stimulation unit.

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Dwg.4/7

US 8324641 A

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Dwg.4/7

Title Terms: NEURAL; NETWORK; BASED; DATA; FUSE; METHOD; TRANSFORM; INFORMATION; SET; TIME; INTERVAL; GEOGRAPHICAL; GRID; ONE; FORMING; SENSE; READ; COMBINATION; FORM; CONSOLIDATE; REPRESENT; ANALYSE; ESTIMATE; CONTACT; OBSERVE; MADE

Derwent Class: S02; T01; W06

International Patent Class (Main): G06E-001/00; G06F-000/00

International Patent Class (Additional): G01S-003/80; G06K-009/00

File Segment: EPI

11/5/12 (Item 7 from file: 350)

DIALOG(R)File 350:Derwent WPIX

010037097 **Image available**

WPI Acc No: 1994-304808/199438

XRPX Acc No: N94-239684

Neural network **for implementing multiple signal processing functions**

- includes plural input, hidden and output processors, with programmable weighting, derived by simulating network response to test patterns .
Patent Assignee: MATSUSHITA ELECTRIC IND CO LTD (MATU); MATSUSHITA ELEC IND CO LTD (MATU); MATSUSHITA DENKI SANGYO KK (MATU); PANASONIC TECHNOLOGIES INC (MATU)
Inventor: ZORTEA A; ZORTEA A E
Number of Countries: 005 Number of Patents: 007
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
EP 618737	A2	19941005	EP 94105148	A	19940331	199438	B
JP 6295293	A	19941021	JP 9464220	A	19940331	199502	
US 5376962	A	19941227	US 9340407	A	19930331	199506	
			US 93119873	A	19930910		
US 5376963	A	19941227	US 9340407	A	19930331	199506	
EP 618737	A3	19960703	EP 94105148	A	19940331	199636	
EP 618737	B1	20000517	EP 94105148	A	19940331	200028	
DE 69424464	E	20000621	DE 624464	A	19940331	200037	
			EP 94105148	A	19940331		

Priority Applications (No Type Date): US 93119873 A 19930910; US 9340407 A 19930331
Cited Patents: No-SR.Pub; 1.Jnl.Ref; EP 384689; EP 551524; US 4803736; US 5025282; US 5161014; WO 9303443
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
EP 618737	A2	E	42	H04N-009/04	
Designated States (Regional): DE GB NL					
JP 6295293	A		36	G06F-015/18	
US 5376962	A		24	H04N-009/64	CIP of application US 9340407
US 5376963	A		21	H04N-009/64	
EP 618737	A3			H04N-009/04	
EP 618737	B1	E		H04N-009/04	
Designated States (Regional): DE GB NL					
DE 69424464	E			H04N-009/04	Based on patent EP 618737

Abstract (Basic): EP 618737 A

A video camera (1310) signal processing system uses a single **neural network** (1420) to implement a number of non-linear functions. For example, the network may carry out gamma correction and contrast compression, with colour and aperture correction being opt. added.

The network is trained (1425) using back-propagation to emulate first one function, then two functions combined, then three combined, etc. The programmed network replaces multiple pipelined processors, operating on signal data sequentially, in more conventional appts. The use of a single network instead of multiple dedicated processors will reduce the engineering development required, and may be of economic benefit to a total system cost.

USE/ADVANTAGE - Economic simplification of signal processing in e.g. video cameras, using programmed **neural network** to implement plural circuit parameter functions, instead of using multiple pipelined processors, with associated signal delays requiring compensation.

Dwg.14b/21

Title Terms: NEURAL; NETWORK; IMPLEMENT; MULTIPLE; SIGNAL; PROCESS; FUNCTION; PLURAL; INPUT; HIDE; OUTPUT; PROCESSOR; PROGRAM; WEIGHT; DERIVATIVE; SIMULATE; NETWORK; RESPOND; TEST; PATTERN
Derwent Class: T01; W04
International Patent Class (Main): G06F-015/18; H04N-009/04; H04N-009/64
International Patent Class (Additional): H04N-005/14
File Segment: EPI

11/5/13 (Item 8 from file: 350)
DIALOG(R)File 350:Derwent WPIX

009996601 **Image available**
WPI Acc No: 1994-264312/199432
Related WPI Acc No: 1994-200477; 1994-264317; 2001-578682

XRPX Acc No: N94-207903
Preprocessing appts. for input data to neural network **- includes time merge device for** reconciling **input data so that it is all on** same time scale
Patent Assignee: PAVILION TECHNOLOGIES INC (PAVI-N)
Inventor: GODBOLE D B; HARTMAN E J; KEELER J D; KEMPF J L; O'HARA S A; OHARA S A
Number of Countries: 022 Number of Patents: 006
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9417482	A1	19940804	WO 94US910	A	19940125	199432 B
AU 9462321	A	19940815	AU 9462321	A	19940125	199444
EP 680637	A1	19951108	EP 94909493	A	19940125	199549
			WO 94US910	A	19940125	
US 5729661	A	19980317	US 92980664	A	19921124	199818
			US 938170	A	19930125	
EP 680637	B1	20010620	EP 94909493	A	19940125	200136
			WO 94US910	A	19940125	
DE 69427524	E	20010726	DE 627524	A	19940125	200150
			EP 94909493	A	19940125	
			WO 94US910	A	19940125	

Priority Applications (No Type Date): US 938170 A 19930125; US 92980664 A 19921124
Cited Patents: 02Jnl.Ref; EP 262647; EP 327268; WO 9217951
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
WO 9417482	A1	E	64	G06F-015/353	
Designated States (National): AU CA JP KP					
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE					
AU 9462321	A				Based on patent WO 9417482
EP 680637	A1	E	64		Based on patent WO 9417482
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LI LU MC NL PT SE					
US 5729661	A		29	G06F-015/18	CIP of application US 92980664
EP 680637	B1	E		G06F-017/17	Based on patent WO 9417482
Designated States (Regional): DE ES FR GB IT NL SE					
DE 69427524	E			G06F-017/17	Based on patent EP 680637
					Based on patent WO 9417482

Abstract (Basic): WO 9417482 A
The preprocessor includes an input buffer for receiving and storing input data, the input data being on different time scales. A time merge device selects a predetermined time scale and **reconciles** the input data stored in the input buffer such that all of the input data is on the **same time scale** . An output device outputs the data **reconciled** by the time **merge** device as **reconciled** data, the **reconciled** data comprising the input data to the system model.
The preprocessor further includes a pre-time merge processor for applying a predetermined algorithm to the input data received by the input buffer prior to input to the time merge device.
ADVANTAGE - Improves training of **neural network** to increase overall network performance.
Dwg.1/20
Title Terms: APPARATUS; INPUT; DATA; NEURAL; NETWORK; TIME; MERGE; DEVICE; INPUT; DATA; SO; TIME; SCALE
Derwent Class: T01
International Patent Class (Main): G06F-015/18; G06F-015/353; G06F-017/17
File Segment: EPI

11/5/14 (Item 9 from file: 350)
DIALOG(R)File 350:Derwent WPIX

009556915 **Image available**
WPI Acc No: 1993-250462/199332

XRPX Acc No: N93-192890

Adaptive process for identification of multi degree of freedom systems - comparing responses of model and system compared to define function for each degree of freedom of system

Patent Assignee: DEUT FORSCH LUFT RAUMFAHRT EV (DELF)
Inventor: MELCHER J
Number of Countries: 002 Number of Patents: 003
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
DE 4202578	A1	19930805	DE 4202578	A	19920130	199332	B
DE 4202578	C2	19950608	DE 4202578	A	19920130	199527	
US 5434773	A	19950718	US 939776	A	19930128	199534	

Priority Applications (No Type Date): DE 4202578 A 19920130
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
DE 4202578	A1		14	G05B-017/02	
DE 4202578	C2		16	G05B-017/02	
US 5434773	A		15	G11C-011/00	

Abstract (Basic): DE 4202578 A

The dynamic characteristic of a linear system is identified by applying an input signal (t) to both the system (1) and a model (2) of the system. The responses of the system (d) and the model (y) are compared and the difference (e) is fed back to adapt the model parameters. The system output is delayed by an element (3).

For each degree of freedom of the multi degree of freedom system a loop is defined for which the model parameters are obtained.

ADVANTAGE - Satisfies real time requirements.

Dwg.1/3

Title Terms: ADAPT; PROCESS; IDENTIFY; MULTI; DEGREE; FREE; SYSTEM; COMPARE ; RESPOND; MODEL; SYSTEM; COMPARE; DEFINE; FUNCTION; DEGREE; FREE; SYSTEM
Derwent Class: T02; T06; U25
International Patent Class (Main): G05B-017/02; G11C-011/00
International Patent Class (Additional): G06G-007/66; H03H-021/00
File Segment: EPI

11/5/16 (Item 11 from file: 350)

DIALOG(R)File 350:Derwent WPIX

001500040
WPI Acc No: 1976-G2962X/197628

Multichannel code-to-time converter - performs linear conversion **generates** one time interval **for positive and negative numbers**

Patent Assignee: NAIMAN V S (NAIM-I)
Number of Countries: 001 Number of Patents: 001
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
SU 484638	A	19751230				197628	B

Priority Applications (No Type Date): SU 1748505 A 19720207

Abstract (Basic): SU 484638 A

The converter is designed for use in telemetry, in timed transmission of digital information. Its new units are number comparison elements (10), OR gates (5) and inverter (8) with the AND gate, series-connected to the inputs of AND gates (3). The converter can perform six different types of conversion, the type of conversion being dictated by the control signal received. For t=f(x), where t-Xm+ **Ai** -Bi-X and O<= X <Xm-Bi, the converter operates in the following way. When control signals C2 and Yk are present, the direct code of X is entered into counter (1), and when X 2h-bk, "1" is entered into the n+1)th digit. The conditions required for the "1" to be entered are produced by comparison element

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File   8:Ei Compendex(R) 1970-2005/Jan W3
         (c) 2005 Elsevier Eng.  Info. Inc.
File  35:Dissertation Abs Online 1861-2005/Jan
         (c) 2005 ProQuest Info&Learning
File  65:Inside Conferences 1993-2005/Feb W1
         (c) 2005 BLDSC all rts. reserv.
File   2:INSPEC 1969-2005/Jan W5
         (c) 2005 Institution of Electrical Engineers
File  94:JICST-EPlus 1985-2005/Dec W4
         (c)2005 Japan Science and Tech Corp(JST)
File 483:Newspaper Abs Daily 1986-2005/Feb 05
         (c) 2005 ProQuest Info&Learning
File   6:NTIS 1964-2005/Jan W5
         (c) 2005 NTIS, Intl Cpyrght All Rights Res
File 144:Pascal 1973-2005/Jan W5
         (c) 2005 INIST/CNRS
File 434:SciSearch(R) Cited Ref Sci 1974-1989/Dec
         (c) 1998 Inst for Sci Info
File  34:SciSearch(R) Cited Ref Sci 1990-2005/Jan W5
         (c) 2005 Inst for Sci Info
File  99:Wilson Appl. Sci & Tech Abs 1983-2004/Dec
         (c) 2005 The HW Wilson Co.
File 583:Gale Group Globalbase(TM) 1986-2002/Dec 13
         (c) 2002 The Gale Group
File 266:FEDRIP 2004/Nov
         Comp & dist by NTIS, Intl Copyright All Rights Res
File  95:TEME-Technology & Management 1989-2005/Jan W1
         (c) 2005 FIZ TECHNIK
File 438:Library Lit. & Info. Science 1984-2005/Dec
         (c) 2005 The HW Wilson Co
File  62:SPIN(R) 1975-2005/Nov W3
         (c) 2005 American Institute of Physics
File 239:Mathsci 1940-2005/Mar
         (c) 2005 American Mathematical Society
File 474:New York Times Abs 1969-2005/Feb 07
         (c) 2005 The New York Times
File 475:Wall Street Journal Abs 1973-2005/Feb 07
         (c) 2005 The New York Times
File 256:TecInfoSource 82-2004/Dec
         (c) 2004 Info.Sources Inc

Set     Items   Description
S1     904917   (NONLINEAR OR NON()LINEAR)(1W)(MODEL? ? OR SYSTEM? ?) OR N-
             EURAL?()(NET? ? OR NETWORK? ?) OR AI OR ARTIFICIAL()INTELLIGE-
             NCE
S2     698884   TIMESCALE? ? OR TIMEBAR? ? OR TIMELINE? ? OR TIME(3N)(SCAL-
             E? ? OR UNIT? ? OR PERIOD? ? OR INTERVAL? ? OR MEASURE? ? OR -
             BAR? ? OR LINE? ? OR SERIES)
S3      42377   (COMMON OR SINGLE OR SINGULAR OR ONE OR MASTER OR TEMPLATE
             OR SAME OR UNIFORM OR CONSISTENT)(5N)S2
S4       1746   S3(15N)(CONFORM? OR RECONCIL? OR ADAPT? OR CONVERT??? OR C-
             ONVERSION OR TRANSLAT? OR TRANSFORM? OR MAP????)
S5       3350   S3(15N)(MERG??? OR COMBIN? OR FUSE? ? OR FUSING OR CHANG???
              OR ADJUST??? OR ADJUSTMENT OR MODIF???? OR MODIFICATION OR A-
             LTER??? OR ALTERATION)
S6     104671   (TRAINING OR "TEST")(1W)(PATTERN? ? OR DATA OR STRING? ? OR
              VECTOR? ?)
S7        185   S1 AND S4
S8        105   S1 AND S5
S9          4   S7:S8 AND S6
S10      54390   PREFILTER? OR PREPROCESS??? OR PRE()(FILTER??? OR PROCESS?-
             ??)
S11          0   S7:S8 AND S10
S12      21969   (COMMON OR SINGLE OR SINGULAR OR ONE OR MASTER OR TEMPLATE
             OR SAME OR UNIFORM OR CONSISTENT)(2W)S2
S13        641   S12(15N)(CONFORM? OR RECONCIL? OR ADAPT? OR CONVERT??? OR -
             CONVERSION OR TRANSLAT? OR TRANSFORM? OR MAP????)
S14       1684   S12(15N)(MERG??? OR COMBIN? OR FUSE? ? OR FUSING OR CHANG?-
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?? OR ADJUST??? OR ADJUSTMENT OR MODIF???? OR MODIFICATION OR ALTER??? OR ALTERATION)

S15	88	S13:S14 AND S1
S16	92	S9 OR S15
S17	62	RD (unique items)
S18	49	S17 NOT PY=2003:2005
S19	4	S6 AND S13:S14
S20	4	S19 NOT S15
S21	4	RD (unique items)
S22	0	S10 AND S13:S14
S23	1730	AU=(FERGUSON, B? OR HARTMAN, E? OR FERGUSON B? OR HARTMAN - E?)
S24	0	S7:S8 AND S23
S25	38	S1 AND S23
S26	2	S25 AND S2
S27	1	S25 AND (S6 OR S10)
S28	3	S26:S27

18/5/2 (Item 2 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

06022767 E.I. No: EIP02126891805
Title: Multivariate time series prediction based on neural networks **applied to stock market**
Author: Yang, Yiwen; Liu, Guizhong
Corporate Source: Dept. of Info. and Commun. Eng. Sch. of Electronic and Info. Eng. Xi'an Jiaotong University, Xi'an 710049, China
Conference Title: 2001 IEEE International Conference on Systems, Man and Cybernetics
Conference Location: Tucson, AZ, United States Conference Date: 20011007-20011010
E.I. Conference No.: 59058
Source: Proceedings of the IEEE International Conference on Systems, Man and Cybernetics v 4 2001. p 2680 (IEEE cat n 01CH37236)
Publication Year: 2001
CODEN: PICYE3 ISSN: 0884-3627
Language: English
Document Type: CA; (Conference Article) Treatment: T; (Theoretical)
Journal Announcement: 0203W4
Abstract: **Neural networks** were used to predict multivariate time series **combined** from Shanghai Stock Exchange (SSE) index. Four related time series were combined as **one** multivariate **time series** . A discrete wavelet **transform** was used to decompose the time series. An approximation to the original time series was obtained and time delay and embedding dimensions were estimated. (Edited abstract) 3 Refs.
Descriptors: *Time series analysis; **Neural networks** ; Wavelet transforms; Approximation theory; Algorithms; Intelligent control
Identifiers: Stock markets
Classification Codes:
723.4.1 (Expert Systems)
922.2 (Mathematical Statistics); 723.4 (Artificial Intelligence); 921.3 (Mathematical Transformations); 921.6 (Numerical Methods); 731.1 (Control Systems)
922 (Statistical Methods); 723 (Computer Software, Data Handling & Applications); 921 (Applied Mathematics); 731 (Automatic Control Principles & Applications)
92 (ENGINEERING MATHEMATICS); 72 (COMPUTERS & DATA PROCESSING); 73 (CONTROL ENGINEERING)

18/5/3 (Item 3 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

05870983 E.I. No: EIP01336613594
Title: Time series forecasting with neural network **ensembles: An application for exchange rate prediction**
Author: Zhang, G.P.; Berardi, V.L.
Corporate Source: Department of Management J Mack Robinson College of Business Georgia State University, Atlanta, GA 30303, United States
Source: Journal of the Operational Research Society v 52 n 6 June 2001. p 652-664
Publication Year: 2001
CODEN: JORSDZ ISSN: 0160-5682
Language: English
Document Type: JA; (Journal Article) Treatment: T; (Theoretical)
Journal Announcement: 0108W3
Abstract: This paper investigates the use of **neural network combining** methods to improve **time series** forecasting performance of the traditional **single** keep-the-best (KTB) model. The ensemble methods are applied to the difficult problem of exchange rate forecasting. Two general approaches to combining **neural networks** are proposed and examined in predicting the exchange rate between the British pound and US dollar. Specifically, we propose to use systematic and serial partitioning methods to build **neural network** ensembles for time series forecasting.

It is found that the basic ensemble approach created with non-varying network architectures trained using different initial random weights is not effective in improving the accuracy of prediction while ensemble models consisting of different **neural network** structures can consistently outperform predictions of the single 'best' network. Results also show that neural ensembles based on different partitions of the data are more effective than those developed with the full **training data** in out-of-sample forecasting. Moreover, reducing correlation among forecasts made by the ensemble members by utilizing data partitioning techniques is the key to success for the neural ensemble models. Although our ensemble methods show considerable advantages over the traditional KTB approach, they do not have significant improvement compared to the widely used random walk model in exchange rate forecasting. 27 Refs.

Descriptors: ***Neura** l **networks** ; Time series analysis; Mathematical models; Operations research; Forecasting

Identifiers: Exchange rate; Keep the best model; **Neural network** ensemble .

Classification Codes:

723.4 (Artificial Intelligence); 922.2 (Mathematical Statistics); 921.6 (Numerical Methods); 912.3 (Operations Research)

723 (Computer Software, Data Handling & Applications); 922 (Statistical Methods); 921 (Applied Mathematics); 912 (Industrial Engineering & Management)

72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS); 91 (ENGINEERING MANAGEMENT)

18/5/4 (Item 4 from file: 8)

DIALOG(R)File 8:Ei Compendex(R)

05732107 E.I. No: EIP00125439499

Title: Tsukamoto-type neural fuzzy inference network

Author: Shoureshi, Rahmat; Hu, Zhi

Corporate Source: Colorado Sch of Mines, Golden, CO, USA

Conference Title: 2000 American Control Conference

Conference Location: Chicago, IL, USA Conference Date: 20000628-20000630

Sponsor: American Automatic Control Council

E.I. Conference No.: 57633

Source: Proceedings of the American Control Conference v 4 2000. IEEE, Piscataway, NJ, USA,00CB36334. p 2463-2467

Publication Year: 2000

CODEN: PRACEO ISSN: 0743-1619

Language: English

Document Type: CA; (Conference Article) Treatment: T; (Theoretical)

Journal Announcement: 0101W4

Abstract: A Tsukamoto-type Neural Fuzzy Inference Network (TNFIN) is proposed in this paper. The TNFIN consists of a special five-layer feedforward neural fuzzy network. The fuzzy implication used in this paper is actually an inverse function transformation rather than the standard linguistic 'if/then' rule. A hybrid learning algorithm combining the Least Square Estimation (LSE) method and the Gradient Descent (GD) method has been used to tune the parameters and speed up the learning process. To demonstrate the capability of the proposed TNFIN, two simulation examples (one in nonlinear function **mapping** and **one** in chaos **time series** prediction) are applied for validating the model. Simulation results show that the TNFIN model with less parameters and smaller iteration numbers produces the remarkable results. (Author abstract) 6 Refs.

Descriptors: *Feedforward **neural networks** ; Fuzzy sets; Membership functions; Inverse problems; Mathematical transformations; Learning algorithms; Least squares approximations; Computer simulation; Chaos theory ; Iterative methods

Identifiers: Neural fuzzy inference network; Least square estimation method; Gradient descent method

Classification Codes:

723.4 (Artificial Intelligence); 921.4 (Combinatorial Mathematics, Includes Graph Theory, Set Theory); 921.3 (Mathematical Transformations);

921.6 (Numerical Methods); 723.5 (Computer Applications); 921.5 (Optimization Techniques)
723 (Computer Software); 921 (Applied Mathematics)
72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS)

18/5/8 (Item 8 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

03577339 E.I. Monthly No: EIM9303-017452
Title: Non - linear system **diagnosis using** neural networks **and fuzzy logic.**
Author: Choi, Jai J.; O'Keefe, Kenneth H.; Baruah, Pranab K.
Conference Title: 1992 IEEE International Conference on Fuzzy Systems - FUZZ-IEEE
Conference Location: San Diego, CA, USA Conference Date: 19920308
Sponsor: IEEE Neural Networks Council
E.I. Conference No.: 17600
Source: 92 IEEE Int Conf Fuzzy Syst FUZZ-IEEE. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA (IEEE cat n 92CH3073-4). p 813-820
Publication Year: 1992
ISBN: 0-7803-0236-2
Language: English
Document Type: PA; (Conference Paper) Treatment: T; (Theoretical); A; (Applications)
Journal Announcement: 9303
Abstract: The authors propose a real-time diagnostic system using a combination of **neural networks** and fuzzy logic. This neuro-fuzzy hybrid system utilizes real-time processing, prediction, and data fusion. A layer of n trained **neural networks** processes n independent time series (channels) which can be contaminated with environmental noise. Each network is trained to predict the future behavior of **one time series** . The prediction error and its rate of **change** from each channel are computed and sent to a fuzzy logic decision output stage, which contains n plus 1 modules. The (n plus 1)th final-output module performs data fusion by combining n individual fuzzy decisions that are tuned to match the domain expert's need. 13 Refs.
Descriptors: ***NEURA** L **NETWORKS** ; FUZZY SETS; EXPERT SYSTEMS; TIME SERIES ANALYSIS; DECISION THEORY; APPLICATIONS
Identifiers: FUZZY LOGIC; **NONLINEAR SYSTEMS** ; **SYSTEM** DIAGNOSIS; REAL-TIME DIAGNOSTIC SYSTEMS; PREDICTION ERROR
Classification Codes:
723 (Computer Software); 921 (Applied Mathematics); 922 (Statistical Methods)
72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS)

18/5/9 (Item 9 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

03354855 E.I. Monthly No: EIM9112-066539
Title: Neural network **modeling of the visual system.**
Author: Vogia, M. J.; Micheli-Tzanakou, Evangelia
Corporate Source: Biomed Eng Dept, Rutgers Univ, Piscataway, NJ, USA
Conference Title: Proceedings of the 12th Annual International Conference of the IEEE Engineering in Medicine and Biology Society
Conference Location: Philadelphia, PA, USA Conference Date: 19901101
E.I. Conference No.: 15414
Source: Biomedical Engineering Perspectives: Health Care Technologies for the 1990's and Beyond Proceedings of the Annual Conference on Engineering in Medicine and Biology pt 3. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA (IEEE cat n 90CH2936-3). p 1425-1426
Publication Year: 1990
CODEN: CEMBAD ISSN: 0589-1019 ISBN: 0-87942-559-8
Language: English
Document Type: PA; (Conference Paper) Treatment: A; (Applications); T;

(Theoretical)
Journal Announcement: 9112
Abstract: A retinal model is introduced that uses **neural networks** to simulate the response of the early visual system to changes in intensity and position of visual stimuli. The model detects any **changes** that may have occurred from **one time period** to the next. The main idea is to investigate how retinal ganglions and higher visual center cells interpret the visual stimulus. A reasonable explanation of how some functions are accomplished in the brain may guide researchers towards an approach to constructing an artificial visual system. The neuronal structures used in the model are the photoreceptors, horizontal, bipolar, amacrine and X, Y, W ganglion cells. In the central part of the retina (fovea) where the acuity is maximum, every output from the photoreceptor cell influences an X, Y or W ganglion call via some predetermined pathway. The X and Y ganglion cell output is then fed to the higher visual relay stations such as the LGN (lateral geniculate nucleus) and the visual cortex, and their output is examined. The model performs well under different stimulus conditions and according to expectations. 3 Refs.
Descriptors: *VISION--*Physiology; BIOMEDICAL ENGINEERING--Ophthalmology; **NEURAL NETWORKS** --Medical Applications
Identifiers: RETINAL MODEL; ARTIFICIAL VISION SYSTEM; NEURONS; GANGLION CELLS; LATERAL GENICULATE NUCLEUS; PHOTORECEPTOR CELLS
Classification Codes:
461 (Biotechnology); 741 (Optics & Optical Devices); 723 (Computer Software)
46 (BIOENGINEERING); 74 (OPTICAL TECHNOLOGY); 72 (COMPUTERS & DATA PROCESSING)

18/5/11 (Item 11 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

02823681 E.I. Monthly No: EIM8911-043679
Title: Neural network **simulation of the early visual system.**
Author: Micheli-Tzanakou, Evangelia; Vogia, Michael J.; Dasey, Timothy J.
Corporate Source: Rutgers Univ, Dep of Biomedical Engineering, Piscataway, NJ, USA
Conference Title: Proceedings of the Annual International Conference of the IEEE Engineering in Medicine and Biology Society
Conference Location: New Orleans, LA, USA Conference Date: 19881104
E.I. Conference No.: 12252
Source: IEEE/Engineering in Medicine and Biology Society Annual Conference Part 3 (of 4). Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA. Available from IEEE Service Cent (cat n 88CH2566-8), Piscataway, NJ, USA. p 1497-1498
Publication Year: 1988
CODEN: IMBPDN
Language: English
Document Type: PA; (Conference Paper) Treatment: X; (Experimental)
Journal Announcement: 8911
Abstract: A retinal model is presented to simulate the response of the early visual system to changes in intensity and position using neuronal networks. The model is capable of identifying a stimulus on a screen and specifying if any **changes** occur from **one time period** (tau) to the next. The neural structures used in the model are the photoreceptor cells, amacrine cells, and the X, Y, and W ganglion cells. The input is made of a N multiplied by N matrix. This retinal model faithfully replicates the receptive field characteristics of the vertebrate early visual system, as well as the general functions of the ganglion cells.
Descriptors: *VISION; SYSTEMS SCIENCE AND CYBERNETICS-- **Neural Nets** ; BIOMEDICAL ENGINEERING--Physiological Models
Identifiers: EARLY VISUAL SYSTEM; RETINAL MODEL; NEURAL STRUCTURES; GANGLION CELLS; PHOTORECEPTOR CELLS
Classification Codes:
461 (Biotechnology); 741 (Optics & Optical Devices)
46 (BIOENGINEERING); 74 (OPTICAL TECHNOLOGY)

18/5/14 (Item 14 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

01123431 E.I. Monthly No: EI8206048268 E.I. Yearly No: EI82021938
Title: REPRESENTATION OF SIMULTANEOUSLY REACHABLE SETS IN NONLINEAR SYSTEMS .
Author: Rudenko, A. V.
Corporate Source: Acad of Sci of the Ukr SSR, Inst of Cybern
Source: Soviet Automatic Control (English translation of Avtomatika) v 13 n 5 Sep-Oct 1980 p 36-41
Publication Year: 1980
CODEN: SAUCBZ ISSN: 0038-5328
Language: ENGLISH
Journal Announcement: 8206
Abstract: A **nonlinear** control **system** of the form dx/dt = f(t, x, u) defined on a finite-dimensional differentiable manifold is considered. Its sets of simultaneous reachability are studied using the simplicial construction proposed by the author, and connected with the invariance of the class of admissible controls with respect to the **common scales** of the **time transformations** . The construction introduced enables us to represent the evolution of the set of simultaneous reachability as a smooth process, that is, a process developing from smooth trajectories. This enables us to investigate various differential characteristics of the evolution process that lead, in particular (to the first order with respect to time), at t = 0 to a differential-geometrical result regarding the convex hull of the original family of vector fields. 13 refs.
Descriptors: *CONTROL SYSTEMS, NONLINEAR--*Theory
Classification Codes:
731 (Automatic Control Principles)
73 (CONTROL ENGINEERING)

18/5/25 (Item 9 from file: 2)
DIALOG(R)File 2:INSPEC

6081981 INSPEC Abstract Number: A9824-8730C-008, C9812-1290L-042
Title: Correlation dimension for two experimental time series
Author(s): Celletti, A.; Bajo Lorenzana, V.M.; Villa, A.E.P.
Author Affiliation: Dipt. di Matematica Pura e Applicata, Univ. L'Aquila, Italy
Journal: Meccanica vol.33, no.4 p.381-96
Publisher: Kluwer Academic Publishers,
Publication Date: Aug. 1998 Country of Publication: Netherlands
CODEN: MECCB9 ISSN: 0025-6455
SICI: 0025-6455(199808)33:4L.381:CDET;1-M
Material Identity Number: P552-98004
U.S. Copyright Clearance Center Code: 0025-6455/98/$12.00
Language: English Document Type: Journal Paper (JP)
Treatment: Theoretical (T); Experimental (X)
Abstract: A method for detecting the dimension of a dynamical system encompassing simultaneously two distinct discrete time series is presented. The time series are provided by the same observable taking distinct and independent initial conditions or they can be formed by realizations of different observables measured simultaneously in a symmetric attractor. The method is derived from an extension of the technique introduced for single time series and allows the common correlation dimension of the chaotic attractor to be evaluated. The correlation dimension associated to two time series is computed for some mathematical models. In particular the two-dimensional standard **mapping** is analysed; a dissipative four-dimensional Henon-like **mapping** is introduced and analyses with **single** and multiple **time series** are performed. The double series method provides a more accurate and efficient evaluation of the embedding and correlation dimensions in all experimental cases. The method is also applied to discrete time series derived from multiple single unit electrophysiological recordings. Several examples of significant dynamics

have been revealed. The results are confirmed by the computation of the (double series) entropy and compared to usual time domain analyses performed in Neuroscience. The double series method is proposed as a complementary method for investigation of dynamical properties of cell assemblies and its potential usefulness for detecting higher order cognitive processes is discussed. (41 Refs)
Subfile: A C
Descriptors: bioelectric phenomena; chaos; cognitive systems; entropy; **neural nets** ; neurophysiology; physiological models; time series; time-domain analysis
Identifiers: time series; correlation dimension; dynamical system; distinct discrete time series; initial conditions; symmetric attractor; chaotic attractor; mathematical models; two-dimensional standard mapping; dissipative four-dimensional Henon-like mapping; double series method; correlation dimensions; discrete time series; multiple single unit electrophysiological recordings; entropy; time domain analyses; neuroscience; complementary method; dynamical properties; cell assemblies; higher order cognitive processes
Class Codes: A8730C (Electrical activity in neurophysiological processes) ; A8728 (Bioelectricity); A8710 (General, theoretical, and mathematical biophysics); A0545 (Theory and models of chaotic systems); A0250 (Probability theory, stochastic processes, and statistics); C1290L (Systems theory applications in biology and medicine); C1140 (Probability and statistics); C1230D (Neural nets)

18/5/29 (Item 13 from file: 2)
DIALOG(R)File 2:INSPEC

4821974 INSPEC Abstract Number: C9412-1230D-066
Title: Connectivity and performance tradeoffs in the cascade correlation learning architecture
Author(s): Phatak, D.S.; Koren, I.
Author Affiliation: Dept. of Electr. Eng., State Univ. of New York, Binghamton, NY, USA
Journal: IEEE Transactions on Neural Networks vol.5, no.6 p.930-5
Publication Date: Nov. 1994 Country of Publication: USA
CODEN: ITNNEP ISSN: 1045-9227
U.S. Copyright Clearance Center Code: 1045-9227/94/$04.00
Language: English Document Type: Journal Paper (JP)
Treatment: Theoretical (T)
Abstract: The cascade correlation is a very flexible, efficient and fast algorithm for supervised learning. It incrementally builds the network by adding hidden **units one** at a **time** , until the desired input/output **mapping** is achieved. It connects all the previously installed units to the new unit being added. Consequently, each new unit in effect adds a new layer and the fan-in of the hidden and output units keeps on increasing as more units get added. The resulting structure could be hard to implement in VLSI, because the connections are irregular and the fan-in is unbounded. Moreover, the depth or the propagation delay through the resulting network is directly proportional to the number of units and can be excessive. We have modified the algorithm to generate networks with restricted fan-in and small depth (propagation delay) by controlling the connectivity. Our results reveal that there is a tradeoff between connectivity and other performance attributes like depth, total number of independent parameters, and learning time. (8 Refs)
Subfile: C
Descriptors: feedforward **neural nets** ; learning (**artificial intelligence**); network topology; parallel architectures
Identifiers: performance tradeoffs; cascade correlation; supervised learning; hidden units; input/output mapping; VLSI implementation; propagation delay; connectivity; learning time; topology
Class Codes: C1230D (Neural nets); C1160 (Combinatorial mathematics)

18/5/42 (Item 3 from file: 34)

05464008 Genuine Article#: WA612 Number of References: 33
Title: PARALLEL CONSENSUAL NEURAL NETWORKS
Author(s): BENEDIKTSSON JA; SVEINSSON JR; ERSOY OK; SWAIN PH
Corporate Source: UNIV ICELAND,ENGN RES INST/IS-107 REYKJAVIK//ICELAND/; PURDUE UNIV,SCH ELECT & COMP ENGN/W LAFAYETTE//IN/47907
Journal: IEEE TRANSACTIONS ON NEURAL NETWORKS, 1997, V8, N1 (JAN), P54-64
ISSN: 1045-9227
Language: ENGLISH Document Type: ARTICLE
Geographic Location: ICELAND; USA
Subfile: Science Citation Index; SciSearch; CC ENGI--Current Contents, Engineering, Technology & Applied Sciences
Journal Subject Category: ENGINEERING, ELECTRICAL & ELECTRONIC; COMPUTER SCIENCE, ARTIFICIAL INTELLIGENCE; COMPUTER SCIENCE, HARDWARE & ARCHITECTURE; COMPUTER SCIENCE, THEORY & METHODS
Abstract: A new type of a **neural - network** architecture, the parallel consensual **neural network** (PCNN), is introduced and applied in classification/data fusion of multisource remote sensing and geographic data. The PCNN architecture is based on statistical consensus theory and involves using stage **neural networks** with transformed input data. The input data are transformed several times and the different transformed data are used as if they were independent inputs. The independent inputs are first classified using the stage **neural networks** . The output responses from the stage networks are then weighted and combined to make a consensual derision. In this paper, optimization methods are used in order to weight the outputs from the stage networks. Two approaches are proposed to compute the data transforms for the PCNN, one for binary data and another for analog data. The analog approach uses wavelet packets. The experimental results obtained with the proposed approach show that the PCNN outperforms both a conjugate-gradient backpropagation **neural network** and conventional statistical methods in terms of overall classification accuracy of **test data** .
Descriptors--Author Keywords: CONSENSUS THEORY ; WAVELET PACKETS ; ACCURACY ; CLASSIFICATION ; PROBABILITY DENSITY ESTIMATION ; STATISTICAL PATTERN RECOGNITION ; TIME-FREQUENCY ANALYSIS ; DATA FUSION
Identifiers--KeyWords Plus: REMOTE-SENSING DATA; CLASSIFICATION; MULTISOURCE; NETS
Research Fronts: 95-2346 002 (ARTIFICIAL **NEURAL - NETWORK** CLASSIFICATION; REMOTELY-SENSED DATA; EFFICIENT CONSTRAINED TRAINING ALGORITHM; LAND-COVER IN MULTISPECTRAL IMAGERY)
95-2039 001 (JUDGMENTAL **TIME - SERIES** FORECASTING; **CONSISTENT** BAYESIAN AGGREGATION; MODEL SELECTION CRITERIA; **NEURAL NETWORKS**)
95-2301 001 (FAST INTEGRAL WAVELET **TRANSFORM** ; DESIGN OF NONUNIFORM COSINE-MODULATED FILTER BANKS; OPTIMAL SIGNAL RECONSTRUCTION)
95-6900 001 (LYAPUNOV EQUATIONS; WORST-CASE IDENTIFICATION; SINGULAR-VALUE DECOMPOSITION; QUOTIENT STRUCTURES IN C-ALGEBRAS; REVERSE FORMS OF A CONVEX MATRIX INEQUALITY)
Cited References:
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18/5/47 (Item 1 from file: 95)

DIALOG(R)File 95:TEME-Technology & Management

00567681 E92053150080

Combining time points and time intervals in a hybrid knowledge representation formalism

(Kombination von Zeitpunkten und Zeitintervallen in einem hybriden Wissensdarstellungsformalismus)

Terenziani, P
Univ. di Torino, I

ISMIS '91, Methodologies for Intelligent Systems, 6th International Symposium, Charlotte, USA, October 16-19, 19911991
Document type: Conference paper Language: English
Record type: Abstract
ISBN: 3-540-54563-8; 0-387-54563-8

ABSTRACT:
In the paper, I illustrate an approach for **combining** time points and time intervals. I propose a **uniform** definition of **time** points and **time intervals** and of the temporal relationships between them, and I describe an integrated temporal reasoner which operates on such a representation. Such a reasoner allows one, among other things, to subdivide an interactable problem (constraint propagation in the general case, in which temporal relations between time points, between time points and time intervals and between time intervals are considered at the same time) into (a possibly intractable number of) smaller tractable subproblems (constraint propagation between time points), and adopts a general heuristic in order to improve efficiency. The paper shows how such an approach has been developed by adopting BACK, a standard Hybrid Knowledge Representation formalism.

DESCRIPTORS: KNOWLEDGE REPRESENTATION; TIME INTERVAL; KNOWLEDGE ENGINEERING ; KNOWLEDGE PROCESSING; **ARTIFICIAL INTELLIGENCE** ; MATHEMATICAL PROOF; LINGUISTICS
IDENTIFIERS: ZEITLICHER BEWEIS; Wissensdarstellung; Formalismus; Zeitintervall

18/5/48 (Item 1 from file: 62)

DIALOG(R)File 62:SPIN(R)

00982921

Phase relationships between two or more interacting processes from one-dimensional time series. I. Basic theory

Janson, N. B. ; Balanov, A. G. ; Anishchenko, V. S. ; McClintock, P. V. E.
Department of Physics, Lancaster University, Lancaster, LA1 4YB, United Kingdom ; Department of Physics, Saratov State University, Astrahanskaya

83, 410026, Saratov, Russia
PHYS. REV. E; 65(3),036211-036211-12 (Mar. 2002) CODEN: PLEEE
Work Type: THEORETICAL; COMPUTING
A general approach is developed for the detection of phase relationships between two or more different oscillatory processes interacting within a single system, using **one** -dimensional **time series** only. It is based on the introduction of angles and radii of return times **maps** , and on studying the dynamics of the angles. An explicit unique relationship is derived between angles and the conventional phase difference introduced earlier for bivariate data. It is valid under conditions of weak forcing. This correspondence is confirmed numerically for a nonstationary process in a forced Van der Pol system. A model describing the angles' behavior for a dynamical system under weak quasiperiodic forcing with an arbitrary number of independent frequencies is derived.

28/5/1 (Item 1 from file: 8)
DIALOG(R)File 8:Ei Compendex(R)

03461226 E.I. Monthly No: EIM9207-040025
Title: Semi-local units for prediction.
Author: **Hartman, Eric** ; Keeler, James D.
Conference Title: International Joint Conference on Neural Networks - IJCNN-91-Seattle Part 2 (of 2)
Conference Location: Seattle, WA, USA Conference Date: 19910708
Sponsor: IEEE Technical Activities Board Council; Int Neural Network Soc
E.I. Conference No.: 16587
Source: Proceedings. IJCNN - International Joint Conference on Neural Networks Int Jt Conf Neural Networks IJCNN 91 Seattle. Publ by IEEE, IEEE Service Center, Piscataway, NJ, USA. p 561-566
Publication Year: 1992
ISBN: 0-7803-0164-1
Language: English
Document Type: PA; (Conference Paper) Treatment: A; (Applications); T; (Theoretical); X; (Experimental)
Journal Announcement: 9207
Abstract: The authors consider a class of semilocal activation functions, which respond to more localized regions of input space than sigmoid functions but less localized regions than radial basis functions (RBFs). In particular, they examine Gaussian bar functions, which sum the Gaussian responses from each input dimension. They present evidence that Gaussian bar networks avoid the slow learning problems of sigmoid networks and deal more robustly with irrelevant inputs than RBF networks. On the Mackey-Glass problem, the speedup over sigmoid networks is so dramatic that the difference in training time between RBF and Gaussian bar networks is minor. Architectures that superpose composed Gaussians (Gaussians-of-Gaussians) to approximate the unknown function have the best performance. An automatic connection pruning mechanism inherent in the Gaussian bar function is very likely a key factor in the success of this representation. 14 Refs.
Descriptors: ***NEURA** L **NETWORKS** --*Applications; STATISTICAL METHODS--**Time Series** Analysis; LEARNING SYSTEMS
Identifiers: ARTIFICIAL **NEURAL NETWORKS** ; NONLINEAR **TIME SERIES** ; GAUSSIAN **BAR** NETWORKS; MACHINE LEARNING; GENERALIZATION CAPABILITY
Classification Codes:
723 (Computer Software); 922 (Statistical Methods)
72 (COMPUTERS & DATA PROCESSING); 92 (ENGINEERING MATHEMATICS)

28/5/2 (Item 1 from file: 35)
DIALOG(R)File 35:Dissertation Abs Online

01129957 ORDER NO: AAD90-31585
A LARGE STORAGE CAPACITY NEURAL NETWORK **CONTENT-ADDRESSABLE MEMORY**
Author: **HARTMAN, ERIC JON**
Degree: PH.D.
Year: 1990
Corporate Source/Institution: THE UNIVERSITY OF TEXAS AT AUSTIN (0227)
Supervisors: CARSTEN PETERSON; ALAN CLINE; VIPIN KUMAR
Source: VOLUME 51/06-B OF DISSERTATION ABSTRACTS INTERNATIONAL. PAGE 2986. 134 PAGES
Descriptors: COMPUTER SCIENCE
Descriptor Codes: 0984

A new **neural network** design is developed and studied for error-correcting content-addressable memory (CAM). Relative to the size of the network, the design enables the storage and error-correcting retrieval of a much larger number of random patterns than has previously been possible. This increase in storage capacity is made possible by using hidden units in an effective manner to assist in the process of storing and retrieving patterns over the visible units. Key components of the design are the Mean-field-theory learning algorithm adapted for CAM (MFT-CAM), an error-correcting retrieval algorithm for use with hidden units, and an

architecture that is not fully connected. In the largest simulation of the design to date, a network with 24 visible units and 84 hidden units stored 1,231 random patterns--more than 10 times the number of units in the network and more than 50 times the number of visible units. Simulation times have limited the amount of data available; under the conservative assumption that the number of stored patterns depends linearly on the number of hidden units, the slope is roughly 20 for networks with 24 visible units. In contrast, the theoretical upper bound on the number of random patterns storable in a Hopfield network in twice the number of units in the network.

The amount of precision required by the connection weights is investigated. It is found that in certain networks the total number of bits necessary to represent the connection weights is less than the number of pattern bits that can be stored and retrieved. In these networks, not quite all of the **training patterns** were perfectly learned and not quite all of the stored patterns are perfectly retrievable.

Error-correcting CAM can also be performed in MFT networks without hidden units; in this case, the error-correcting performance is shown to be somewhat superior to that of the widely studied Bidirectional-perceptron algorithm. It is shown that the Bidirectional-perceptron algorithm is a special limit of the MFT-CAM algorithm.

28/5/3 (Item 1 from file: 2)
DIALOG(R)File 2:INSPEC

04100448 INSPEC Abstract Number: C9204-1230D-047
Title: Predicting the future: advantages of semilocal units
Author(s): **Hartman, E.** ; Keeler, J.D.
Author Affiliation: Microelectronics & Comp. Technol. Corp., Austin, TX, USA
Journal: Neural Computation vol.3, no.4 p.566-78
Publication Date: Winter 1991 Country of Publication: USA
CODEN: NEUCEB ISSN: 0899-7667
Language: English Document Type: Journal Paper (JP)
Treatment: Theoretical (T)
Abstract: In investigating gaussian radial basis function (RBF) networks for their ability to model nonlinear **time series** , it was found that while RBF networks are much faster than standard sigmoid unit backpropagation for low-dimensional problems, their advantages diminish in high-dimensional input spaces. This is particularly troublesome if the input space contains irrelevant variables. The authors suggest that this limitation is due to the localized nature of RBFs. To gain the advantages of the highly nonlocal sigmoids and the speed advantages of RBFs, they propose a particular class of semilocal activation functions that is a natural interpolation between these two families. They present evidence that networks using these gaussian bar units avoid the slow learning problem of sigmoid unit networks and, very importantly, are more accurate than RBF networks in the presence of irrelevant inputs. The authors postulate that an interesting behavior displayed by gaussian bar functions under gradient descent dynamics which they call automatic connection pruning, is an important factor in the success of this representation. (18 Refs)
Subfile: C
Descriptors: digital simulation; **neural nets** ; **time series**
Identifiers: semilocal units; gaussian radial basis function; nonlinear **time series** ; RBF networks; standard sigmoid unit backpropagation; low-dimensional problems; high-dimensional input spaces; nonlocal sigmoids; semilocal activation functions; gaussian bar units; sigmoid unit networks; gaussian bar functions; gradient descent dynamics; automatic connection pruning

File 348:EUROPEAN PATENTS 1978-2005/Jan W05
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File 349:PCT FULLTEXT 1979-2002/UB=20050203,UT=20050127
(c) 2005 WIPO/Univentio

Set	Items	Description
S1	63879	(NONLINEAR OR NON()LINEAR)(1W)(MODEL? ? OR SYSTEM? ?) OR N-EURAL?()(NET? ? OR NETWORK? ?) OR AI OR ARTIFICIAL()INTELLIGE-NCE
S2	394128	TIMESCALE? ? OR TIMEBAR? ? OR TIMELINE? ? OR TIME(3N)(SCAL-E? ? OR UNIT? ? OR PERIOD? ? OR INTERVAL? ? OR MEASURE? ? OR -BAR? ? OR LINE? ? OR SERIES)
S3	29217	(COMMON OR SINGLE OR SINGULAR OR ONE OR MASTER OR TEMPLATE OR SAME OR UNIFORM OR CONSISTENT)(2W)S2
S4	1172	S3(15N)(CONFORM? OR RECONCIL? OR ADAPT? OR CONVERT??? OR C-ONVERSION OR TRANSLAT? OR TRANSFORM? OR MAP????)
S5	2203	S3(15N)(MERG??? OR COMBIN? OR FUSE? ? OR FUSING OR CHANG??? OR ADJUST??? OR ADJUSTMENT OR MODIF???? OR MODIFICATION OR A-LTER??? OR ALTERATION)
S6	2620	(TRAINING OR 239,240)(1W)(PATTERN? ? OR DATA OR STRING? ? -OR VECTOR? ?)
S7	16364	("TEST")(1W)(PATTERN? ? OR DATA OR STRING? ? OR VECTOR? ?)
S8	20162	PREFILTER? OR PREPROCESS??? OR PRE()(FILTER??? OR PROCESS?-??)
S9	22	S4:S5(50N)S1
S10	19	S4:S5(50N)S6:S8
S11	32	S9:S10

T/3,K/3-7,9,10,11,17-21,23-26,32

11/3,K/3 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00934804
Meteorological radar precipitation pattern prediction method and apparatus
Verfahren und Vorrichtung zur Niederschlagsmustervoraussage mit einem meteorologischen Radar
Methode et systeme pour la prediction de la forme de la precipitation avec un radar meteorologique
PATENT ASSIGNEE:
NIPPON TELEGRAPH AND TELEPHONE CORPORATION, (686333), 19-2, Nishi-Shinjuku 3-chome, Shinjuku-ku, Tokyo 160, (JP), (applicant designated states: AT;BE;CH;DE;DK;ES;FI;FR;GB;GR;IE;IT;LI;LU;MC;NL;PT;SE)
INVENTOR:
Sakaino, Hidetomo, 7-5-403, Greenhaitsu, Yokosuka-shi, Kanagawa-ken, (JP)
Suzuki, Satoshi, 1-50-12, Tomioka-nishi, Kanazawa-ku, Yokohama-shi, Kanagawa-ken, (JP)
Horikoshi, Tsutomu, 1-8-18-105, Maruyamadai, Konan-ku, Yokohama-shi, Kanagawa-ken, (JP)
LEGAL REPRESENTATIVE:
Poulin, Gerard et al (17984), BREVALEX 3, rue du docteur Lancereaux, 75008 Paris, (FR)
PATENT (CC, No, Kind, Date): EP 851240 A2 980701 (Basic)
EP 851240 A3 990602
APPLICATION (CC, No, Date): EP 97402626 971104;
PRIORITY (CC, No, Date): JP 96347684 961226; JP 9729126 970213; JP 9729127 970213; JP 9729128 970213; JP 9764665 970318; JP 9799053 970416; JP 97183986 970709
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS: G01S-013/95; G06T-007/20;
ABSTRACT WORD COUNT: 207

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	9827	4015
SPEC A	(English)	9827	16713
Total word count - document A			20728
Total word count - document B			0
Total word count - documents A + B			20728

...SPECIFICATION to represent a mapping relationship between time-series-arranged two-dimensional images using weight coefficients of a **neural network** . However, the best suited structure of the **neural network** cannot be easily estimated and only a predicted image as an average of learned patterns can be obtained. More specifically, a feedforward network, **one** of representative **time** -□series□learning models, has the ability to **map** a spatial pattern, but a time-series pattern cannot be learned by using such a network (refer to "Brain and **Neural Network** " edited by S. Amari, et al., Asakura Publishing). That is, only prediction on conditions of fixed gray...

11/3,K/4 (Item 4 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

h gc c c g

00651095
HIGH PRECISION ON-LINE DYNAMIC SIGNATURE VERIFICATION SYSTEM
DYNAMISCHES HOCHPRAZISIONS-ON-LINE-UNTERSCHRIFTPRUFUNGSSYSTEM
SYSTEME DE VERIFICATION DYNAMIQUE DE SIGNATURES, EN LIGNE, DE HAUTE PRECISION
PATENT ASSIGNEE:
LANDAU, Alexander, (1848610), 1475 Folsom Avenue, Apartment 378, Boulder, CO 80302, (US), (Proprietor designated states: all)
SHRAIRMAN, Ruth, Dr., (1848620), 1475 Folsom Avenue, Apartment 378, Boulder, CO 80303, (US), (Proprietor designated states: all)
INVENTOR:
LANDAU, Alexander, 1475 Folsom Avenue, Apartment 378, Boulder, CO 80302, (US)
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LEGAL REPRESENTATIVE:
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PATENT (CC, No, Kind, Date): EP 746825 A1 961211 (Basic)
EP 746825 A1 961218
EP 746825 B1 030226
WO 94020926 940915
APPLICATION (CC, No, Date): EP 93908312 930304; WO 93US2234 930304
PRIORITY (CC, No, Date): EP 93908312 930304; WO 93US2234 930304
DESIGNATED STATES: DE; ES; FR; GB; IT; SE
INTERNATIONAL PATENT CLASS: G06K-009/00; G06K-009/46; G06K-009/36; G06K-009/66
NOTE:
No A-document published by EPO
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200309	1651
CLAIMS B	(German)	200309	1775
CLAIMS B	(French)	200309	1887
SPEC B	(English)	200309	3821
Total word count - document A			0
Total word count - document B			9134
Total word count - documents A + B			9134

...SPECIFICATION evaluating signals matching, both the reference dynamic data and the to-be-verified dynamic data signals are **pre - processed** to eliminate different kinds of time distortions, so that the signals can be compared as though both sets of data were stationary signals. More particularly, the compared signals are reduced to the **same time scale** or to the same average velocity and by this to **adjust** a frequency coincidence between the signals. In order to eliminate phase shifts, a special "sliding window", method...

11/3,K/5 (Item 5 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00645846
METHOD AND APPARATUS FOR PREPROCESSING INPUT DATA TO A NEURAL NETWORK
VERFAHREN UND ANLAGE ZUR EINGANGSDATENVORVERARBEITUNG FUR EIN NEURONALES NETZWERK
PROCEDE ET APPAREIL DE PRETRAITEMENT DES DONNEES INTRODUITES DANS UN RESEAU

NEURONAL

PATENT ASSIGNEE:
PAVILION TECHNOLOGIES INC., (1744350), 3500 West Balcones Center Drive, Austin, TX 78759, (US), (Proprietor designated states: all)
INVENTOR:
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HARTMAN, Eric, J., 8902 Slayton Road, Austin, TX 78753, (US)
O'HARA, Steven, A., 3006 Fox Hollow, Round Rock, TX 78681, (US)
KEMPF, Jill, L., 2410-B Sharon Lane, Austin, TX 78703, (US)
GODBOLE, Devendra, B., 12403 Copperfield Drive, Austin, Texas 78753, (US)
LEGAL REPRESENTATIVE:
Lawrence, Malcolm Graham (47878), Hepworth, Lawrence, Bryer & Bizley Merlin House Falconry Court Baker's Lane, Epping Essex CM16 5DQ, (GB)
PATENT (CC, No, Kind, Date): EP 680637 A1 951108 (Basic)
EP 680637 B1 010620
WO 9417482 940804
APPLICATION (CC, No, Date): EP 94909493 940125; WO 94US910 940125
PRIORITY (CC, No, Date): US 8170 930125
DESIGNATED STATES: DE; ES; FR; GB; IT; NL; SE
INTERNATIONAL PATENT CLASS: G06F-017/17
NOTE:
No A-document published by EPO
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200125	1333
CLAIMS B	(German)	200125	1222
CLAIMS B	(French)	200125	1509
SPEC B	(English)	200125	10180
Total word count - document A			0
Total word count - document B			14244
Total word count - documents A + B			14244

...SPECIFICATION data is complete for all time samples for both time interval "1" and time interval "2".

The **neural network** models that are utilized for time-series prediction and control require that the time-interval between successive **training patterns** be constant. Since the data that comes in from real-world systems is not always on the **same time scale** , it is desirable to time- **merge** the data before it can be used for training or running the **neural network** model. To achieve this time-merge operation, it may be necessary to extrapolate, interpolate, average or compress...bad data is "cut" from the data set, as will be described hereinbelow. The operation in the **preprocessing** mode fills in this bad data and then time merges it. In this example, the time scale for x1))(t) is utilized as a time scale for the time **merge** data such that the time **merge** data x1))'(t) is on the **same time scale** with the "cut" value filled in as a result of the **preprocessing** operation and the data set x2))(t) is processed in accordance with one of the time **merged** algorithms to provide data for x2))'(t) and on the **same time scale** as the data x1))'(t). These algorithms will be described in more detail hereinbelow.

Referring now to FIGURE 6, there is illustrated a flowchart depicting the **preprocessing** operation. The flow chart is initiated at a start block 70 and then proceeds to a decision...

...CLAIMS the input data stored in the input buffer to place all of the input data on the **same time scale** ; and
an output device (44) for outputting the data **reconciled** (xD))' (t)) by the time **merge** device as reconciled data, said reconciled data

comprising the input data to the system model.
2. The data **preprocessor** of Claim 1, and further comprising a pre-time merge processor for applying a predetermined algorithm to...

11/3,K/6 (Item 6 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00645733
PREDICTIVE NETWORKS AND METHOD WITH LEARNED PREPROCESSING PARAMETERS
VORAUSSCHAUENDE NETZWERKE UND VERFAHREN MIT GELERNTEN VORARBEITUNGSPARAMETERS
RESEAUX PREDICTIFS ET METHODE AVEC PARAMETRES DE PRETRAITEMENT APPRIS
PATENT ASSIGNEE:
PAVILION TECHNOLOGIES INC., (1744350), 3500 West Balcones Center Drive, Austin, TX 78759, (US), (Proprietor designated states: all)
INVENTOR:
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HARTMAN, Eric, J., 8902 Slayton Road, Austin, TX 78753, (US)
O'HARA, Steven, A., 3006 Fox Hollow, Round Rock, TX 78681, (US)
KEMPF, Jill, L., 2410-B Sharon Lane, Austin, TX 78703, (US)
GODBOLE, Devendra, B., 6805 Wood Hollow Drive, 228, Austin, TX 78731, (US)
LEGAL REPRESENTATIVE:
Lawrence, Malcolm Graham (47878), Hepworth, Lawrence, Bryer & Bizley Merlin House Falconry Court Baker's Lane, Epping Essex CM16 5DQ, (GB)
PATENT (CC, No, Kind, Date): EP 680639 A1 951108 (Basic)
EP 680639 B1 000405
WO 9417489 940804
APPLICATION (CC, No, Date): EP 94907888 940125; WO 94US905 940125
PRIORITY (CC, No, Date): US 8218 930125
DESIGNATED STATES: DE; ES; FR; GB; IT; NL; SE
INTERNATIONAL PATENT CLASS: G06F-015/80
NOTE:
No A-document published by EPO
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200014	1393
CLAIMS B	(German)	200014	1216
CLAIMS B	(French)	200014	1717
SPEC B	(English)	200014	10435
Total word count - document A			0
Total word count - document B			14761
Total word count - documents A + B			14761

...SPECIFICATION and, in the training mode, the data preprocessor is controlled by the input device and the determined **preprocessing** parameters generated thereby.
In yet another embodiment of the present invention, the data **preprocessor** is comprised of an input buffer for receiving and storing the received data, the received data being on different time scales. A time merge device is operable to select a predetermined time scale and **reconcile** the received data such that all the received data is placed on the **same time scale** . An output device then outputs the data **reconciled** by the time **merge** device as the□preprocessed□data. The data **preprocessor** further includes a pre-time merge processor for applying a predetermined algorithm to the received data prior...data is complete for all time samples for both time interval "1" and time

preprocessed being on different time scales;
selecting a predetermined time scale and time merging the data stored in the input buffer such that all of the time **merged** data is on the **same time scale** ; and
outputting the **time merged** data as the **preprocessed** data.
18. The method of Claim 15, wherein the step of operating the data **preprocessor** in both the runtime mode and the training mode comprises:
receiving and storing data to be **preprocessed** ;
selecting portions of the stored data to be **preprocessed** and introducing a predetermined amount of delay therein to output delay data; and
outputting the undelayed and...

11/3,K/7 (Item 7 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00633481
OPERATING A NEURAL NETWORK WITH MISSING AND/OR INCOMPLETE DATA
BETREIBEN EINES NEURONALEN NETZWERKS MIT FEHLENDEN UND/ODER INKOMPLETTEN DATEN
EXPLOITATION D'UN RESEAU NEURONAL PRESENTANT DESONNEES MANQUANTES ET/OU INCOMPLETES
PATENT ASSIGNEE:
PAVILION TECHNOLOGIES INC., (1744350), 3500 West Balcones Center Drive, Austin, TX 78759, (US), (Proprietor designated states: all)
INVENTOR:
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FERGUSON, Ralph Bruce, 9815 Copper Creek, Apartment 814, Austin, TX 78729, (US)
LEGAL REPRESENTATIVE:
Lawrence, Malcolm Graham (47878), Hepworth, Lawrence, Bryer & Bizley Merlin House Falconry Court Baker's Lane, Epping Essex CM16 5DQ, (GB)
PATENT (CC, No, Kind, Date): EP 671038 A1 950913 (Basic)
EP 671038 B1 030514
WO 94012948 940609
APPLICATION (CC, No, Date): EP 94903278 931119; WO 93US11251 931119
PRIORITY (CC, No, Date): US 980664 921124
DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE
INTERNATIONAL PATENT CLASS: G06F-015/80
NOTE:
No A-document published by EPO
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200320	2002
CLAIMS B	(German)	200320	1633
CLAIMS B	(French)	200320	2223
SPEC B	(English)	200320	6184
Total word count - document A			0
Total word count - document B			12042
Total word count - documents A + B			12042

...SPECIFICATION data is complete for all time samples for both time interval 1 and time interval 2.
The **neural network** models that are utilized for time-series

prediction and control require that the time-interval between successive **training patterns** be constant. Since the data that comes in from real-world systems is not always on the **same time scale**, it is desirable to time- **merge** the data before it can be used for training or running the **neural network** model. To achieve this time-merge operation, it may be necessary to extrapolate, interpolate, average or compress...

11/3,K/9 (Item 9 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

00498590
NEURAL NETWORKS
NEURONALES NETZWERK
RESEAUX NEURONAUX

PATENT ASSIGNEE:
BRITISH TELECOMMUNICATIONS public limited company, (846100), 81 Newgate Street, London EC1A 7AJ, (GB), (applicant designated states: AT;BE;CH;DE;DK;ES;FR;GB;GR;IT;LI;LU;NL;SE)

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SCOTT, Edward, Geoffrey, 56A The Laurels Aldham Road, Hadleigh Suffolk IP7 6BS, (GB)
WOOD, David, Charles, Morden Villa Melton Road, Woodbridge Suffolk IP12 1AX, (GB)
HEALEY, Peter, 31 Norbury Road Ipswich, Suffolk IP4 4RQ, (GB)
WEBB, Roderick, Peter, Shimaker Cottage Stone Common Blaxhall, Woodbridge Suffolk IP12 2DP, (GB)

LEGAL REPRESENTATIVE:
Read, Matthew Charles et al (47911), Venner Shipley & Co. 20 Little Britain, London EC1A 7DH, (GB)

PATENT (CC, No, Kind, Date): EP 502943 A1 920916 (Basic)
EP 502943 B1 990203
WO 9107714 910530

APPLICATION (CC, No, Date): EP 91900297 901120; WO 90GB1782 901120

PRIORITY (CC, No, Date): GB 8926183 891120; GB 9003443 900215; GB 9024332 901108

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IT; LI; LU; NL; SE

INTERNATIONAL PATENT CLASS: G06E-003/00; G06F-015/76;

NOTE:
No A-document published by EPO

LANGUAGE (Publication,Procedural,Application): English; English; English

FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	9905	540
CLAIMS B	(German)	9905	493
CLAIMS B	(French)	9905	611
SPEC B	(English)	9905	3434
Total word count - document A			0
Total word count - document B			5078
Total word count - documents A + B			5078

...SPECIFICATION addressed independently to adjust the weights.
The bias voltages which determine the threshold levels can also be **adjusted** at the **same** slower□timescale□in these implementations.
Measuring the similarity of the whole of the output set and target set and...

...global measure rather than on the similarity of pairs of individual vectors provides enhanced training rates for **neural networks** having a data throughput rate that can be higher than the rate at which the response determining...

11/3,K/10 (Item 10 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00437956
A system for learning an external evaluation standard
System zum Lernen eines externen Auswertungsstandards
Systeme pour apprendre un standard d'evaluation externe
PATENT ASSIGNEE:
FUJITSU LIMITED, (211460), 1015, Kamikodanaka, Nakahara-ku, Kawasaki-shi, Kanagawa 211, (JP), (applicant designated states: DE;FR;GB)
INVENTOR:
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Saga, Kazushige, 2-19-10, Asakusabashi, Taito-ku, Tokyo, 111, (JP)
Sekiguchi, Minoru, 4-19-61-202, Tamagawa-Gakuen, Machida-shi, Tokyo, 194, (JP)
Nagata, Shigemi, 1-1-16, Kachida-Minami, Kohoku-ku, Yokohama-shi, Kanagawa, 223, (JP)
LEGAL REPRESENTATIVE:
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PATENT (CC, No, Kind, Date): EP 434423 A2 910626 (Basic)
EP 434423 A3 930203
EP 434423 B1 960228
APPLICATION (CC, No, Date): EP 90314012 901220;
PRIORITY (CC, No, Date): JP 89328401 891220
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS: G06F-015/80;
ABSTRACT WORD COUNT: 190

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB96	1271
CLAIMS B	(German)	EPAB96	1134
CLAIMS B	(French)	EPAB96	1427
SPEC B	(English)	EPAB96	5217
Total word count - document A			0
Total word count - document B			9049
Total word count - documents A + B			9049

...CLAIMS is met.
5. The system for learning the external evaluation standard according to claim 4 wherein

said **neural network** processing means includes means for receiving the input pattern including a current input pattern and at least one past input pattern, obtained as a result of said input means **converting** the external information at a current period and the external information at least at **one** past **period time** .

6. The system for learning the external evaluation standard according to claim 1, wherein

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said **neural network** internal evaluation means comprises means for receiving the evaluation input pattern including a current input pattern and...

11/3,K/11 (Item 11 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00345162
Decoding of barcodes by preprocessing scan data.
Strichkodedekodierung durch Vorverarbeitung von Abtastdaten.
Decodage de codes a barres par pretraitement de donnees de balayage.
PATENT ASSIGNEE:
MICROSCAN SYSTEMS INCORPORATED, (1115190), 939 Industry Drive, Tukwila Washington 98188, (US), (applicant designated states: CH;DE;FR;GB;IT;LI;NL;SE)
INVENTOR:
Mertel, Michael E., 18309, 155th Place S.E., Renton, Washington 98058, (US)
Thomas, James E., 14135, 233rd Place S.E., Issaquah, Washington 98027, (US)
LEGAL REPRESENTATIVE:
Baillie, Iain Cameron et al (27951), c/o Ladas & Parry Altheimer Eck 2, D-80331 Munchen, (DE)
PATENT (CC, No, Kind, Date): EP 346938 A2 891220 (Basic)
EP 346938 A3 900418
EP 346938 B1 931124
APPLICATION (CC, No, Date): EP 89111014 890616;
PRIORITY (CC, No, Date): US 208736 880617
DESIGNATED STATES: CH; DE; FR; GB; IT; LI; NL; SE
INTERNATIONAL PATENT CLASS: G06K-007/14; G06K-007/016;
ABSTRACT WORD COUNT: 155

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1438
CLAIMS B	(German)	EPBBF1	1275
CLAIMS B	(French)	EPBBF1	1533
SPEC B	(English)	EPBBF1	5324
Total word count - document A			0
Total word count - document B			9570
Total word count - documents A + B			9570

...SPECIFICATION to count states. Instead of a binary sequenced output it has multiple discrete outputs that become active **in** sequence as the input is toggled, thus indicating how many pulses have been received. With this general...

...in mind the relationship of the bar counter 17 the 10 bit multiplexer 11 and the log **conversion** and margin detecting can be explained in more detail. Each **time a pulse is** received the logic sequencers 42 stops the bar/space counter 17, latches the count and starts the...

11/3,K/17 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01033021 **Image available**

SYSTEM AND METHOD FOR PRE-PROCESSING INPUT DATA TO A SUPPORT VECTOR MACHINE

SYSTEME ET PROCEDE POUR PRETRAITER DES DONNEES D'ENTREE SUR UNE MACHINE A VECTEUR DE SUPPORT

Patent Applicant/Assignee:
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HARTMAN Eric, 12703 Foxhound Cove, Austin, TX 78729, US,
Legal Representative:
MEYERTONS HOOD KIVLIN KOWERT & GOETZEL P C (agent), HOOD, Jeffrey C., P.O. Box 398, Austin, TX 78767, US,

Patent and Priority Information (Country, Number, Date):
Patent: WO 200363016 A1 20030731 (WO 0363016)
Application: WO 2003US1582 20030117 (PCT/WO US0301582)
Priority Application: US 200251574 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 27560

Fulltext Availability:
Detailed Description
Claims

Detailed Description

... A data preprocessor may be provided for preprocessing received (i.e., input) data in accordance with predetermined **preprocessing** parameters to output **preprocessed** data. The data **preprocessor** may include an input buffer for receiving and storing the input data. The input data may be...

...time scales. A time merge device may be operable to select a predetern-fined time scale and **reconcile** the input data so that all of the input data are placed on the **same time scale** . An output device may output the **reconciled** data from the time **merge** device as **preprocessed** data. ...information may indicate production costs related to increased energy expenses, for example. Thus, in one embodiment, the **preprocessor** may be operable to detect and remove and/or replace outlying data in an input data set...

...the support vector machine.

Various embodiments of the systems and methods described above may thus operate to **preprocess** input data for a support vector machine to **reconcile** data on different **time scales** to a□common time scale□ . Various embodiments of the systems and methods may also operate to remove and/or replace bad or missing data in the input data. The resulting **preprocessed** input data may then be used to train and/or

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the method further comprising:
training the support vector machine according to a predetermined training algorithm applied to...other;
time merge the input data for the inputs such that all of the input data are **reconciled** to the **same time scale** ; and output the□reconciled□ time **merged** data as **reconciled** data, the□reconciled□data comprising the input data to the support vector machine.

69 The carrier medium of claim 68, wherein the support vector machine comprises a **non - linear model** having a set of model parameters defining a representation of a system, said model parameters capable of being trained;
and
wherein the input data comprise **training data** including target input data and target output data, wherein said reconciled data comprise reconciled **training data** including reconciled target input data and reconciled target output data, and wherein said **reconciled** target input data and **reconciled** target output data are both based on a **common time scale** ;
wherein said program instructions are further executable to:
train the support vector machine according to a predetermined...

11/3,K/18 (Item 4 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01033020 **Image available**
SYSTEM AND METHOD FOR PRE-PROCESSING INPUT DATA TO A NON-LINEAR MODEL FOR USE IN ELECTRONIC COMMERCE
SYSTEME ET PROCEDE DE PRETRAITEMENT DE DONNEES D'ENTREE EN MODELE NON LINEAIRE DESTINE A ETRE UTILISE DANS LE COMMERCE ELECTRONIQUE
Patent Applicant/Assignee:
PAVILION TECHNOLOGIES INC, 11100 Metric Boulevard, Suite 700, Austin, TX 78758, US, US (Residence), US (Nationality)
Inventor(s):
FERGUSON Bruce, 903 Morning View Place, Round Rock, TX 78664, US,
HARTMAN Eric, 12703 Foxhound Cove, Austin, TX 78729, US,
Legal Representative:
HOOD Jeffrey C (agent), Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., P.O. Box 398, Austin, TX 78767, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200363015 A1 20030731 (WO 0363015)
Application: WO 2003US1520 20030117 (PCT/WO US0301520)
Priority Application: US 200251421 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English

h gc c c g

wherein the input electronic commerce data comprise run-time...input electronic commerce data for the inputs such that all of the input electronic commerce
data are **reconciled** to the **same time scale** ; and
output the **reconciled** time **merged** electronic commerce data as **reconciled** electronic commerce data, the **reconciled** electronic commerce data comprising the input electronic commerce data to the **non - linear model** .

69 The carrier medium of claim 68, wherein the **non - linear model** includes a set of model parameters defining a representation of the electronic commerce system, said model parameters...and reconciled target output electronic commerce data, and wherein said reconciled target input electronic commerce data and **reconciled** target output electronic commerce data are both based on a **common time scale** ;
wherein said program instructions are further executable to:
train the **non - linear model** according to a predetermined training algorithm applied to said reconciled target input electronic commerce data and said reconciled target output electronic commerce data to develop model parameter values such that said **non - linear model** has stored therein a representation of the electronic commerce system that

11/3,K/19 (Item 5 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01032964 **Image available**
SYSTEM AND METHOD FOR OPERATING A NON-LINEAR MODEL WITH MISSING DATA FOR USE IN ELECTRONIC COMMERCE
SYSTEME ET PROCEDE PERMETTANT DE METTRE EN OEUVRE UN MODELE NON LINEAIRE AVEC DES DONNEES MANQUANTES A UTILISER DANS LE COMMERCE ELECTRONIQUE
Patent Applicant/Assignee:
PAVILION TECHNOLOGIES INC, 11100 Metric Boulevard, Suite 700, Austin, TX 78758, US, US (Residence), US (Nationality)
Inventor(s):
FERGUSON Bruce, 903 Morning View Place, Round Rock, TX 78664, US,
HARTMAN Eric, 12703 Foxhound Cove, Austin, TX 78729, US,
Legal Representative:
HOOD Jeffrey C (agent), Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., P.O. Box 398, Austin, TX 78767, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200362952 A2-A3 20030731 (WO 0362952)
Application: WO 2003US1521 20030117 (PCT/WO US03001521)
Priority Application: US 200251598 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG
SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 20166

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stored therein a representation of the system that generated the target output data in response to...

11/3,K/20 (Item 6 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

01032954 **Image available**
PRE-PROCESSING INPUT DATA WITH OUTLIER VALUES FOR A SUPPORT VECTOR MACHINE
PRETRAITEMENT DE DONNEES D'ENTREE PRESENTANT DES VALEURS ABERRANTES POUR UNE MACHINE A VECTEUR DE SUPPORT
Patent Applicant/Assignee:
PAVILION TECHNOLOGIES INC, 11100 Metric Boulevard, Suite 700, Austin, TX 78758, US, US (Residence), US (Nationality)
Inventor(s):
FERGUSON Bruce, 903 Morning View Place, Round Rock, TX 78664, US,
HARTMAN Eric, 12703 Foxhound Cove, Austin, TX 78729, US,
Legal Representative:
MEYERTONS HOOD KIVLIN KOWERT & GOETZEL P C (agent), HOOD, Jeffrey C., P.O. Box 398, Austin, TX 78767, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200362940 A1 20030731 (WO 0362940)
Application: WO 2003US1372 20030117 (PCT/WO US0301372)
Priority Application: US 200251266 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG
SI SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 23487

Fulltext Availability:
Detailed Description

Detailed Description
... A data preprocessor may be provided for preprocessing received (i.e., input) data in accordance with predetermined **preprocessing** parameters to output **preprocessed** data. The data **preprocessor** may include an input buffer for receiving and storing the input data. The input data may be...

...different time scales. A time merge device may be operable to select a predetermined time scale and **reconcile** the input data so that all of the input data are placed on the **same time scale** . An output device may output the **reconciled** data from the time **merge** device as **preprocessed** data. ...information may indicate production costs related to increased energy expenses, for example. Thus, in one embodiment, the **preprocessor** may be operable to detect and remove and/or replace outlying data in an input data set...

...the support vector machine.

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Various embodiments of the systems and methods described above may thus operate to **preprocess** input data for a support vector machine to **reconcile** data on different **time scales** to a□common time scale□ . Various embodiments of the systems and methods may also operate to remove and/or replace bad or missing data in the input data. The resulting **preprocessed** input data ...being bad, and is therefore "cut" from the data set, as described below. In this example, the **preprocessing** operation fills in, i.e., replaces, this bad data and then time merges the data, as shown...

...this example, the time scale for x1(t) is utilized as a time scale for the time **merge** data such that the time **merge** data x',(t) is on the **same time scale** with the "cut" value filled in as a result of the **preprocessing** operation and the data set X2(t) is processed in accordance with one of the time **merged** algorithms to provide data for X'2(t) and on the **same time scale** as the data ...described in more detail below.

Figure 9A is a high level flowchart depicting one embodiment of a **preprocessing** operation for **preprocessing** input data to a support vector machine. It should be noted that in other embodiments, various of ...data to the support vector machine.

In one embodiment, the received input data of 904 may comprise **training data** which includes target input data and target output data. The reconciled data may comprise reconciled **training data** which includes **reconciled** target input data and **reconciled** target output data which are both based on a **common time scale** (or other common scale).

In one embodiment, the support vector machine may be operable to be trained...

11/3,K/21 (Item 7 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00844377 **Image available**
ADAPTIVE LEARNING SYSTEM AND METHOD
SYSTEME ET TECHNIQUE D'APPRENTISSAGE ADAPTATIF
Patent Applicant/Assignee:
UNIVERSITY OF OTAGO, Leith Street, Dunedin, NZ, NZ (Residence), NZ (Nationality), (For all designated states except: US)
Patent Applicant/Inventor:
KASABOV Nikola Kirilov, 8 Spylaw Street, Maori Hill, Dunedin, NZ, NZ (Residence), NZ (Nationality), (Designated only for: US)
Legal Representative:
WEST-WALKER Gregory James (et al) (agent), A J Park, Huddart Parker Building, Post Office Square, Wellington, NZ,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200178003 A1 20011018 (WO 0178003)
Application: WO 2001NZ59 20010410 (PCT/WO NZ0100059)
Priority Application: NZ 503882 20000410
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM

h gc c c g

TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 11506

Fulltext Availability:
Detailed Description

Detailed Description
... online evolving from one pass data propagation. The system is also arranged to learn time series that **change** their dynamics through time and never repeat **same** patterns. **Time series** processes with **changing** dynamics could be of different origins, for example biological, environmental, industrial processes control, financial. The system could also be used for off-line training and testing similar to other standard **neural network** techniques.
An example of learning a complex chaotic function is described with reference to Figures 18A and...

11/3,K/23 (Item 9 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

00779570 **Image available**
NEURAL NETWORK RADAR PROCESSOR
PROCESSEUR RADAR DE RESEAU NEURAL
Patent Applicant/Assignee:
AUTOMOTIVE SYSTEMS LABORATORY INC, 27200 Haggerty Road, Suite B-12, Farmington Hills, MI 48331, US, US (Residence), -- (Nationality)
Inventor(s):
FARMER Michael E, 5119 Lake Bluff Road, W. Bloomfield, MI 48323, US,
JACOBS Craig S, 24762 Roosevelt Court, Farmington Hills, MI 48335, US,
CONG Shan, 37300 Medallion Court #6-105, Farmington, MI 48331, US,
Legal Representative:
BEGIN Laurence C (et al) (agent), Lyon, P.C., Suite 207, 3883 Telegraph Road, Bloomfield Hills, MI 48302-1476, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200113141 A2-A3 20010222 (WO 0113141)
Application: WO 2000US22007 20000811 (PCT/WO US0022007)
Priority Application: US 99148597 19990812
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
JP
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
Publication Language: English
Filing Language: English
Fulltext Word Count: 10564

Fulltext Availability:
Detailed Description
Claims

Detailed Description
... of target ranges are not overlapping.

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In accordance with a third aspect, a method of training a **neural network** in a **neural network** radar processor comprises forming at least **one** first **time series** of in-phase and quadrature-phase components representing a down- **converted** radar return signal from a target space and applying the at least **one** first **time series** to an input layer of a **neural network** . Each component comprises an associated waveform and the at least one first time series comprises a plurality...of said fourth plurality of output nodes tend towards a nullity.

12. A method of training a **neural network** in a **neural network** radar processor as recited in claim 11, wherein said operation of forming at least one first time series comprises forming at least **one** first **time series** of in-phase and quadrature-phase components representing a down- **converted** radar return signal from a target space.

13. A method of training a **neural network** in a **neural network** radar processor as recited in

Claim
corresponding weight values comprises a back propagation process.

14 A method of training a **neural** network in a neural network radar processor as recited in any of claims 11 through 13, wherein...

...in a neural network radar processor as recited in any of claims 11 through 14, wherein said **neural network** further comprises at least one function with at least one parameter, ftirther comprising the operation of **adjusting** said at least one parameter so that for said at least **one** first **time series** selected from a□time series□with a leakage signal but no target, a time series with a DC bias but no target ...
...of said fourth plurality of output nodes tend towards a nullity.

16 A method of training a **neural network** in a **neural network** radar processor as recited in any of claims 11 through 15, wherein for said at least **one time series** representative of a target space having a target at a range, said operation of **adjusting** said third plurality of corresponding weight values further comprises adjusting said third plurality of corresponding weight values...

...is present at a range corresponding to said other output nodes.

17 A method of training a **neural network** in a **neural network** radar processor as recited in any of claims 1 1 through 16, wherein said **neural network** further comprises at least one function with at least one parameter, ftirther comprising the operation of **adjusting** said at least one parameter so that for said at least **one time series** representative of a target space having a target at a range, said operation of **adjusting** said at least one parameter further comprises adjusting said at least one parameter so that said output...

11/3,K/24 (Item 10 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00777020
A SYSTEM, METHOD AND ARTICLE OF MANUFACTURE FOR RESOURCE ADMINISTRATION IN AN E-COMMERCE TECHNICAL ARCHITECTURE

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SYSTEME, PROCEDE ET ARTICLE MANUFACTURE POUR L'ADMINISTRATION DE RESSOURCES DANS UNE ARCHITECTURE TECHNIQUE DE COMMERCE ELECTRONIQUE

Patent Applicant/Assignee:
ACCENTURE LLP, Parkstraat 83, NL-2514 JG 'S Gravenhage, NL, NL (Residence), NL (Nationality), (For all designated states except: US)
Patent Applicant/Inventor:
UNDERWOOD Roy A, 4436 Hearthmoor Court, Long Grove, IL 60047, US, US (Residence), US (Nationality), (Designated only for: US)
Legal Representative:
HICKMAN Paul L (agent), Oppenheimer Wolff & Donnelly, LLP, P.O. Box 52037, Palo Alto, CA 94303-0746, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200109791 A2-A3 20010208 (WO 0109791)
Application: WO 2000US20547 20000728 (PCT/WO US0020547)
Priority Application: US 99364161 19990730
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG US UZ VN YU ZA ZW
(EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 136396

Fulltext Availability:
Detailed Description

Detailed Description
... If data is going to be shared with an existing application, attempts should be made to reuse **test data** from the legacy system.

Use the existing data store capabilities to extract or massage the data into a format that is ilyintegrated into the new application.
easi 1
0 Create **one - time** extract and formatting applications to extract the legacy data, perform formatting and business operations, and import the newly **modified** data into the new data store.

The ReTA Component Test Workbook Plan-Prep provides the mechanism for maintaining component **test data** required during test execution. When creating the **test data** , all attempts should be made to make the **test data** reusable.

Test Planning
Description
The test planning ftinction during a ReTA engagement provides an opportunity to define...

11/3,K/25 (Item 11 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

00523527

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SYSTEM AND METHOD FOR ACCESSING AND MANIPULATING TIME-BASED DATA
PROCEDE ET SYSTEME POUR MANIPULER DES DONNEES TEMPORELLES
Patent Applicant/Assignee:
AVID TECHNOLOGY INC,
GAGNE Rejean,
Inventor(s):
GAGNE Rejean,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9954879 A1 19991028
Application: WO 99CA313 19990413 (PCT/WO CA9900313)
Priority Application: US 9863289 19980421
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AU CA JP US AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
Publication Language: English
Fulltext Word Count: 6686

English Abstract
...based data allows data of at least two diverse types to be arranged with respect to a **common** internal **time line** of a meta-clip. The internal time line of the meta-clip is re- **mapped** , in use, to a global time line in a **nonlinear** editing **system** . The data within the meta-clip is accessed, modified and otherwise manipulated within the **non - linear** editing **system** environment as a single clip. A meta-clip can comprise diverse data types including, without limitation, video...

11/3,K/26 (Item 12 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

00406184 **Image available**
3-BRAIN ARCHITECTURE FOR AN INTELLIGENT DECISION AND CONTROL SYSTEM
ARCHITECTURE A TROIS CERVEAUX POUR SYSTEME INTELLIGENT DE COMMANDE ET DE DECISION
Patent Applicant/Assignee:
WERBOS Paul J,
Inventor(s):
WERBOS Paul J,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9746929 A2 19971211
Application: WO 97US9724 19970604 (PCT/WO US9709724)
Priority Application: US 9619154 19960604
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GE HU IL
IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT
RO RU SD SE SG SI SK TJ TM TR TT UA UG US UZ VN GH KE LS MW SD SZ UG AM
AZ BY KG KZ MD RU TJ TM AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT
SE BF BJ CF CG CI CM GA GN ML MR NE SN TD TG
Publication Language: English
Fulltext Word Count: 84125

Fulltext Availability:
Claims

Claim
... natural to train layer V as a "dual-use" structure,

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making decisions and reconstructing reality at the **same time** ,
and learning based on the sum of feedbacks from both
activities. Alternatively, one might ascribe the decision...I + (Mn)) (I
+ (Mn) 1) (I+Mn)
(15)
Using this approach, after only n steps of calculation, **one**
%% sees" 2' **periods** of **time** into the future.
There are two ways to implement this approach:

1 For each number j, from...

11/3,K/32 (Item 18 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2005 WIPO/Univentio. All rts. reserv.

00190367 **Image available**
NEURAL NETWORKS
RESEAUX NEURONAUX
Patent Applicant/Assignee:
BRITISH TELECOMMUNICATIONS PUBLIC LIMITED COMPANY,
REJMAN-GREENE Marek Andrzej Zbigniew,
SCOTT Edward Geoffrey,
WOOD David Charles,
HEALEY Peter,
WEBB Roderick Peter,
Inventor(s):
REJMAN-GREENE Marek Andrzej Zbigniew,
SCOTT Edward Geoffrey,
WOOD David Charles,
HEALEY Peter,
WEBB Roderick Peter,
Patent and Priority Information (Country, Number, Date):
Patent: WO 9107714 A1 19910530
Application: WO 90GB1782 19901120 (PCT/WO GB9001782)
Priority Application: GB 8926183 19891120; GB 903443 19900215; GB 9024332
19901108
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
AT AU BE CA CH DE DK ES FR GB GB GR IT JP KR LU NL SE US
Publication Language: English
Fulltext Word Count: 4097

Fulltext Availability:
Detailed Description

Detailed Description
... addressed independently to adjust the weights.

The bias voltages which determine the threshold levels can
also be **adjusted** at the **same** slower□timescale□in these
implementations,
Measuring ...measure
rather than on the similarity of pairs of individual vectors
provides enhanced training rates f or **neural networks** having a
data throughput rate that can be higher than the rate at which
the response determining...
?

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File 347:JAPIO Nov 1976-2004/Sep(Updated 050204)
         (c) 2005 JPO & JAPIO
File 350:Derwent WPIX 1963-2005/UD,UM &UP=200509
         (c) 2005  Thomson Derwent
File 348:EUROPEAN PATENTS 1978-2005/Jan W05
         (c) 2005 European Patent Office
File 349:PCT FULLTEXT 1979-2002/UB=20050203,UT=20050127
         (c) 2005 WIPO/Univentio

Set     Items   Description
S1        182   AU=(FERGUSON B? OR HARTMAN E?)
S2     730499   TIMESCALE? ? OR TIMEBAR? ? OR TIMELINE? ? OR TIME(3N)(SCAL-
              E? ? OR UNIT? ? OR PERIOD? ? OR INTERVAL? ? OR MEASURE? ? OR -
              BAR? ? OR LINE? ? OR SERIES)
S3      40441   (COMMON OR SINGLE OR SINGULAR OR ONE OR MASTER OR TEMPLATE
              OR SAME OR UNIFORM OR CONSISTENT)(2W)S2
S4         12   S1 AND S3
```

4/5/1 (Item 1 from file: 350)
DIALOG(R)File 350:Derwent WPIX

015647964 **Image available**
WPI Acc No: 2003-710147/200367
XRPX Acc No: N03-567730
Data preprocessor e.g. personal computer for support vector machine, selects predetermined time scale and reconciles stored input data such that all input data for all inputs are on same time scale
Patent Assignee: PAVILION TECHNOLOGIES INC (PAVI-N); FERGUSON B (FERG-I); HARTMAN E (HART-I)
Inventor: **FERGUSON B ; HARTMAN E**
Number of Countries: 102 Number of Patents: 003
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
US 20030139828	A1	20030724	US 200251574	A	20020118	200367	B
WO 200363016	A1	20030731	WO 2003US1582	A	20030117	200367	
AU 2003207607	A1	20030902	AU 2003207607	A	20030117	200422	

Priority Applications (No Type Date): US 200251574 A 20020118
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
US 20030139828	A1		63	G06E-001/00	
WO 200363016	A1	E		G06F-015/18	

Designated States (National): AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
Designated States (Regional): AT BE BG CH CY CZ DE DK EA EE ES FI FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SI SK SL SZ TR TZ UG ZM ZW

AU 2003207607	A1			G06F-015/18	Based on patent WO 200363016

Abstract (Basic): US 20030139828 A1
NOVELTY - The input buffers (156,158,160,162) store input data associated with different time scales relative to each other. A time merge unit selects a predetermined time scale and reconciles the stored input data, such that all input data for all inputs are on the **same time scale** . An output device outputs the reconciled data which comprises the input data to a support vector machine.
DETAILED DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:
(1) method for preprocessing input data; and
(2) carrier medium storing program for preprocessing input data.
USE - Data preprocessor e.g. personal computer system, mainframe computer system, workstation, network appliance, internet appliance, personal digital assistant (PDA) and television system for use with support vector machine, for e- commerce, financial market, scientific, medical and manufacturing applications.
ADVANTAGE - Since the input data for all the inputs are on the **same time scale** , missing of data is avoided.
DESCRIPTION OF DRAWING(S) - The figure shows a diagrammatic view of the computer system.
input buffers (156,158,160,162)
pp; 63 DwgNo 16/23
Title Terms: DATA; PERSON; COMPUTER; SUPPORT; VECTOR; MACHINE; SELECT; PREDETERMINED; TIME; SCALE; STORAGE; INPUT; DATA; INPUT; DATA; INPUT; TIME; SCALE

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Derwent Class: T01
International Patent Class (Main): G06E-001/00; G06F-015/18
International Patent Class (Additional): G05B-013/02; G06E-001/000; G06E-003/00; G06G-007/00
File Segment: EPI

4/5/2 (Item 2 from file: 350)
DIALOG(R)File 350:Derwent WPIX

009996601 **Image available**
WPI Acc No: 1994-264312/199432
Related WPI Acc No: 1994-200477; 1994-264317; 2001-578682
XRPX Acc No: N94-207903
Preprocessing appts. for input data to neural network - includes time merge device for reconciling input data so that it is all on same time scale
Patent Assignee: PAVILION TECHNOLOGIES INC (PAVI-N)
Inventor: GODBOLE D B; **HARTMAN E J** ; KEELER J D; KEMPF J L; O'HARA S A; OHARA S A
Number of Countries: 022 Number of Patents: 006
Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week	
WO 9417482	A1	19940804	WO 94US910	A	19940125	199432	B
AU 9462321	A	19940815	AU 9462321	A	19940125	199444	
EP 680637	A1	19951108	EP 94909493	A	19940125	199549	
			WO 94US910	A	19940125		
US 5729661	A	19980317	US 92980664	A	19921124	199818	
			US 938170	A	19930125		
EP 680637	B1	20010620	EP 94909493	A	19940125	200136	
			WO 94US910	A	19940125		
DE 69427524	E	20010726	DE 627524	A	19940125	200150	
			EP 94909493	A	19940125		
			WO 94US910	A	19940125		

Priority Applications (No Type Date): US 938170 A 19930125; US 92980664 A 19921124
Cited Patents: 02Jnl.Ref; EP 262647; EP 327268; WO 9217951
Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
WO 9417482	A1	E	64	G06F-015/353	
Designated States (National): AU CA JP KP					
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE					
AU 9462321	A				Based on patent WO 9417482
EP 680637	A1	E	64		Based on patent WO 9417482
Designated States (Regional): AT BE CH DE DK ES FR GB GR IE IT LI LU MC NL PT SE					
US 5729661	A		29	G06F-015/18	CIP of application US 92980664
EP 680637	B1	E		G06F-017/17	Based on patent WO 9417482
Designated States (Regional): DE ES FR GB IT NL SE					
DE 69427524	E			G06F-017/17	Based on patent EP 680637
					Based on patent WO 9417482

Abstract (Basic): WO 9417482 A
The preprocessor includes an input buffer for receiving and storing input data, the input data being on different time scales. A time merge device selects a predetermined time scale and reconciles the input data stored in the input buffer such that all of the input data is on the

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same time scale . An output device outputs the data reconciled by the time merge device as reconciled data, the reconciled data comprising the input data to the system model.

The preprocessor further includes a pre-time merge processor for applying a predetermined algorithm to the input data received by the input buffer prior to input to the time merge device.

ADVANTAGE - Improves training of neural network to increase overall network performance.

Dwg.1/20

Title Terms: APPARATUS; INPUT; DATA; NEURAL; NETWORK; TIME; MERGE; DEVICE; INPUT; DATA; SO; TIME; SCALE
Derwent Class: T01
International Patent Class (Main): G06F-015/18; G06F-015/353; G06F-017/17
File Segment: EPI

4/5/3 (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00645846
METHOD AND APPARATUS FOR PREPROCESSING INPUT DATA TO A NEURAL NETWORK
VERFAHREN UND ANLAGE ZUR EINGANGSDATENVORVERARBEITUNG FUR EIN NEURONALES NETZWERK
PROCEDE ET APPAREIL DE PRETRAITEMENT DES DONNEES INTRODUITES DANS UN RESEAU NEURONAL

PATENT ASSIGNEE:
PAVILION TECHNOLOGIES INC., (1744350), 3500 West Balcones Center Drive, Austin, TX 78759, (US), (Proprietor designated states: all)

INVENTOR:
KEELER, James, David, 12701 Shemya Cove, Austin, TX 78729, (US)
HARTMAN, Eric, J. , 8902 Slayton Road, Austin, TX 78753, (US)
O'HARA, Steven, A., 3006 Fox Hollow, Round Rock, TX 78681, (US)
KEMPF, Jill, L., 2410-B Sharon Lane, Austin, TX 78703, (US)
GODBOLE, Devendra, B., 12403 Copperfield Drive, Austin, Texas 78753, (US

LEGAL REPRESENTATIVE:
Lawrence, Malcolm Graham (47878), Hepworth, Lawrence, Bryer & Bizley Merlin House Falconry Court Baker's Lane, Epping Essex CM16 5DQ, (GB)

PATENT (CC, No, Kind, Date): EP 680637 A1 951108 (Basic)
EP 680637 B1 010620
WO 9417482 940804
APPLICATION (CC, No, Date): EP 94909493 940125; WO 94US910 940125
PRIORITY (CC, No, Date): US 8170 930125
DESIGNATED STATES: DE; ES; FR; GB; IT; NL; SE
INTERNATIONAL PATENT CLASS: G06F-017/17
CITED PATENTS (EP B): EP 262647 A; EP 327268 A; WO 92/17951 A
CITED REFERENCES (EP B):
IJCNN INTERNATIONAL JOINT CONFERENCE ON NEURAL NETWORKS vol. 1 , 19 June 1989 , WASHINGTON , USA pages 781 - 786 TAM 'A model for temporal correlation of biological neuronal spike trains'
ICASSP INTERNATIONAL CONFERENCE ON ACOUSTICS, SPEECH AND SIGNAL PROCESSING vol. 1 , 14 May 1991 , TORONTO , CANADA pages 105 - 108 HAFFNER 'Integrating time alignment and neural networks for high performance continuous speech recognition';
NOTE:
No A-document published by EPO
LEGAL STATUS (Type, Pub Date, Kind, Text):
Change: 010321 A1 International Patent Classification changed: 20010127
Examination: 20000322 A1 Date of dispatch of the first examination

h gc c c

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                          report: 19981222
 Lapse:           040121 B1 Date of lapse of European Patent in a
                            contracting state (Country, date):  ES
                            20011220, NL 20010620,
 Oppn None:       020612 B1 No opposition filed: 20020321
 Grant:           010620 B1 Granted patent
 Lapse:           030219 B1 Date of lapse of European Patent in a
                            contracting state (Country, date):  NL
                            20010620,
 Application:     941109 A  International application (Art. 158(1))
 Application:     951108 A1 Published application (A1with Search Report
                            ;A2without Search Report)
 Examination:     951108 A1 Date of filing of request for examination:
                            950707
 Change:          951129 A1 Inventor (change)
 Change:          960605 A1 Designated Contracting States (change)
 Examination:     990203 A1 Date of despatch of first examination report:
                            981222
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text  Language    Update     Word Count
      CLAIMS B  (English)   200125      1333
      CLAIMS B   (German)   200125      1222
      CLAIMS B   (French)   200125      1509
      SPEC B    (English)   200125     10180
Total word count - document A           0
Total word count - document B          14244
Total word count - documents A + B     14244
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4/5/4 (Item 2 from file: 348)

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DIALOG(R)File 348:EUROPEAN PATENTS
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00645733
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PREDICTIVE NETWORKS AND METHOD WITH LEARNED PREPROCESSING PARAMETERS
VORAUSSCHAUENDE NETZWERKE UND VERFAHREN MIT GELERNTEN VORARBEITUNGSPARAMETERS
RESEAUX PREDICTIFS ET METHODE AVEC PARAMETRES DE PRETRAITEMENT APPRIS

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PATENT ASSIGNEE:
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    Austin, TX 78759, (US), (Proprietor designated states: all)
INVENTOR:
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  KEMPF, Jill, L., 2410-B Sharon Lane, Austin, TX 78703, (US)
  GODBOLE, Devendra, B., 6805 Wood Hollow Drive,  228, Austin, TX 78731,
    (US
LEGAL REPRESENTATIVE:
  Lawrence, Malcolm Graham (47878), Hepworth, Lawrence, Bryer & Bizley
    Merlin House Falconry Court Baker's Lane, Epping Essex CM16 5DQ, (GB)
PATENT (CC, No, Kind, Date):  EP 680639  A1  951108 (Basic)
                              EP 680639  B1  000405
                              WO 9417489  940804
APPLICATION (CC, No, Date):   EP 94907888 940125;  WO 94US905  940125
PRIORITY (CC, No, Date): US 8218 930125
DESIGNATED STATES: DE; ES; FR; GB; IT; NL; SE
INTERNATIONAL PATENT CLASS: G06F-015/80
CITED PATENTS (EP B): US 5150313 A
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h gc c c

CITED REFERENCES (EP B):
NEURAL NETWORKS vol. 4, no. 2 , 1991 , OXFORD GB pages 185 - 191 LEVIN 'Neural network architecture for adaptive system modeling and control'
IJCNN INTERNATIONAL JOINT CONFERENCE ON NEURAL NETWORKS vol. 2 , 8 July 1991 , SEATTLE , USA pages 675 - 681 TROUDET 'Towards practical control design using neural computation'
IJCNN INTERNATIONAL JOINT CONFERENCE ON NEURAL NETWORKS vol. 2 , 17 June 1990 , SAN DIEGO , USA pages 569 - 574 BEERHOLD 'Pulse-processing neural net hardware with selectable topology and adaptive weights and delays'
ICASSP-92 IEEE INTERNATIONAL CONFERENCE ON ACOUSTICS, SPEECH AND SIGNAL PROCESSING vol. 2 , 23 March 1992 , SAN FRANCISCO , USA pages 285 - 288 RANDER 'Learning the time-delay characteristics in a neural network';
NOTE:
No A-document published by EPO
LEGAL STATUS (Type, Pub Date, Kind, Text):

Oppn None:	010321	B1	No opposition filed: 20010106
Grant:	20000405	B1	Granted patent
Lapse:	030219	B1	Date of lapse of European Patent in a contracting state (Country, date): ES 20000405, NL 20000405, SE 20000705,
Lapse:	010627	B1	Date of lapse of European Patent in a contracting state (Country, date): SE 20000705,
Lapse:	020626	B1	Date of lapse of European Patent in a contracting state (Country, date): ES 20000405, SE 20000705,
Application:	941109	A	International application (Art. 158(1))
Application:	951108	A1	Published application (A1with Search Report ;A2without Search Report)
Examination:	951108	A1	Date of filing of request for examination: 950707
Change:	990210	A1	Title of invention (German) (change)
Change:	990210	A1	Title of invention (English) (change)
Change:	990210	A1	Title of invention (French) (change)
Change:	990324	A1	Title of invention (German) (change)
Change:	990324	A1	Title of invention (English) (change)
Change:	990324	A1	Title of invention (French) (change)
Examination:	990428	A1	Date of despatch of first examination report: 990312

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200014	1393
CLAIMS B	(German)	200014	1216
CLAIMS B	(French)	200014	1717
SPEC B	(English)	200014	10435
Total word count - document A			0
Total word count - document B			14761
Total word count - documents A + B			14761

4/5/5 (Item 3 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS

00633481
OPERATING A NEURAL NETWORK WITH MISSING AND/OR INCOMPLETE DATA
BETREIBEN EINES NEURONALEN NETZWERKS MIT FEHLENDEN UND/ODER INKOMPLETTEN DATEN

h gc c c

EXPLOITATION D'UN RESEAU NEURONAL PRESENTANT DESONNEES MANQUANTES ET/OU INCOMPLETES

PATENT ASSIGNEE:
PAVILION TECHNOLOGIES INC., (1744350), 3500 West Balcones Center Drive, Austin, TX 78759, (US), (Proprietor designated states: all)

INVENTOR:
KEELER, James, David, 12701 Shemya Cove, Austin, TX 78729, (US)
HARTMAN, Eric, Jon , 8902 Slayton, Austin, TX 78753, (US)
FERGUSON, Ralph Bruce, 9815 Copper Creek, Apartment 814, Austin, TX 78729 , (US

LEGAL REPRESENTATIVE:
Lawrence, Malcolm Graham (47878), Hepworth, Lawrence, Bryer & Bizley Merlin House Falconry Court Baker's Lane, Epping Essex CM16 5DQ, (GB)

PATENT (CC, No, Kind, Date): EP 671038 A1 950913 (Basic)
EP 671038 B1 030514
WO 94012948 940609

APPLICATION (CC, No, Date): EP 94903278 931119; WO 93US11251 931119

PRIORITY (CC, No, Date): US 980664 921124

DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; GR; IE; IT; LI; LU; MC; NL; PT; SE

INTERNATIONAL PATENT CLASS: G06F-015/80

CITED PATENTS (EP B): EP 436916 A; US 5052043 A

CITED REFERENCES (EP B):
IEEE TRANSACTIONS ON NEURAL NETWORKS vol. 3, no. 4 , July 1992 , NEW YORK US pages 624 - 627 LEONARD 'Using radial basis functions to approximate a function and its error bounds'
INTERNATIONAL JOURNAL OF PATTERN RECOGNITION AND ARTIFICIAL INTELLIGENCE ED. WORLD SCIENTIFIC PUBLISHING COMPANY SINGAPORE vol. 6, no. 4 , October 1992 pages 539 - 569 KOUTSOUGERAS 'A feedforward neural network classifier model : multiple classes, confidence output values, and implementation'
PROCEEDINGS OF THE 1991 INTERNATIONAL CONFERENCE ON ARTIFICIAL NEURAL NETWORKS ICANN-91 vol. 1 , 24 June 1991 , ESPOO , FINLAND pages 233 - 238 WHITE 'Confidence-constrained optimization for robust learning';

NOTE:
No A-document published by EPO

LEGAL STATUS (Type, Pub Date, Kind, Text):

Change:	020327 A1	Title of invention (German) changed: 20020204
Application:	940914 A	International application (Art. 158(1))
Lapse:	050119 B1	Date of lapse of European Patent in a contracting state (Country, date): AT 20030514, BE 20030514, CH 20030514, LI 20030514, DK 20030814, ES 20030825, GB 20031119, GR 20030814, IE 20031119, LU 20031119, MC 20031130, NL 20030514, PT 20030814, SE 20030814,
Lapse:	041103 B1	Date of lapse of European Patent in a contracting state (Country, date): AT 20030514, BE 20030514, CH 20030514, LI 20030514, DK 20030814, ES 20030825, GB 20031119, GR 20030814, LU 20031119, NL 20030514, PT 20030814, SE 20030814,
Lapse:	040707 B1	Date of lapse of European Patent in a contracting state (Country, date): AT 20030514, BE 20030514, CH 20030514, LI 20030514, DK 20030814, ES 20030825, GR 20030814, NL 20030514, PT 20030814, SE 20030814,
Oppn None:	040506 B1	No opposition filed: 20040217
Lapse:	040107 B1	Date of lapse of European Patent in a

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                              contracting state (Country, date):  CH
                              20030514, LI 20030514, GR 20030814, NL
                              20030514, PT 20030814, SE 20030814,
 Lapse:           031210 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  SE
                              20030814,
 Change:          020710 A1 Title of invention (French) changed: 20020522
 Change:          020710 A1 Title of invention (English) changed: 20020522
 Change:          020710 A1 Title of invention (German) changed: 20020522
 Change:          020327 A1 Title of invention (English) changed: 20020204
 Change:          020327 A1 Title of invention (French) changed: 20020204
 Grant:           030514 B1 Granted patent
 Lapse:           040102 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  CH
                              20030514, LI 20030514, SE 20030814,
 Lapse:           040114 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  AT
                              20030514, CH 20030514, LI 20030514, ES
                              20030825, GR 20030814, NL 20030514, PT
                              20030814, SE 20030814,
 Lapse:           040602 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  AT
                              20030514, CH 20030514, LI 20030514, DK
                              20030814, ES 20030825, GR 20030814, NL
                              20030514, PT 20030814, SE 20030814,
 Lapse:           040922 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  AT
                              20030514, BE 20030514, CH 20030514, LI
                              20030514, DK 20030814, ES 20030825, GR
                              20030814, LU 20031119, NL 20030514, PT
                              20030814, SE 20030814,
 Lapse:           040922 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  AT
                              20030514, BE 20030514, CH 20030514, LI
                              20030514, DK 20030814, ES 20030825, GR
                              20030814, LU 20031119, NL 20030514, PT
                              20030814, SE 20030814,
 Lapse:           050112 B1 Date of lapse of European Patent in a
                              contracting state (Country, date):  AT
                              20030514, BE 20030514, CH 20030514, LI
                              20030514, DK 20030814, ES 20030825, GB
                              20031119, GR 20030814, LU 20031119, MC
                              20031130, NL 20030514, PT 20030814, SE
                              20030814,
 Application:     950913 A1 Published application (A1with Search Report
                              ;A2without Search Report)
 Examination:     950913 A1 Date of filing of request for examination:
                              950522
 Examination:     981028 A1 Date of despatch of first examination report:
                              980911
 Change:          990707 A1 Title of invention (German) (change)
 Change:          990707 A1 Title of invention (English) (change)
 Change:          990707 A1 Title of invention (French) (change)
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text  Language    Update     Word Count
      CLAIMS B  (English)   200320      2002
      CLAIMS B   (German)   200320      1633
      CLAIMS B   (French)   200320      2223
      SPEC B    (English)   200320      6184
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h gc c c

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Total word count - document A           0
Total word count - document B       12042
Total word count - documents A + B  12042
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4/5/6 (Item 1 from file: 349)

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DIALOG(R)File 349:PCT FULLTEXT
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01033021    **Image available**
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SYSTEM AND METHOD FOR PRE-PROCESSING INPUT DATA TO A SUPPORT VECTOR MACHINE
SYSTEME ET PROCEDE POUR PRETRAITER DES DONNEES D'ENTREE SUR UNE MACHINE A VECTEUR DE SUPPORT

Patent Applicant/Assignee:
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HARTMAN Eric , 12703 Foxhound Cove, Austin, TX 78729, US
Legal Representative:
MEYERTONS HOOD KIVLIN KOWERT & GOETZEL P C (agent), HOOD, Jeffrey C., P.O. Box 398, Austin, TX 78767, US,

Patent and Priority Information (Country, Number, Date):
Patent: WO 200363016 A1 20030731 (WO 0363016)
Application: WO 2003US1582 20030117 (PCT/WO US0301582)
Priority Application: US 200251574 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Main International Patent Class: G06F-015/18
International Patent Class: G06E-001/00
Publication Language: English
Filing Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 27560

English Abstract
A system and method for preprocessing input data to a support vector machine (SVM). The SVM is a system model having parameters that define the representation of the system being modeled, and operates in two modes: runtime and training. A data preprocessor preprocesses received data in accordance with predetermined preprocessing parameters, and outputs preprocessed data. The data preprocessor includes an input buffer for receiving and storing the input data. The input data may be on different time scales. A time merge device determines a desired time scale and reconciles the input data so that all of the input data are placed on the desired time scale. An output device outputs the reconciled data from the time merged device as preprocessed data. The reconciled data may be input to the SVM in training mode to train the SVM, and/or in

run-time mode to generate control parameters and/or predictive output information (FIG. 3A, 10,12,14,20,22).

French Abstract
La presente invention concerne un systeme et un procede pour pretraiter des donnees d'entree sur une machine a vecteur de support (SVM). Cette SVM est un modele de systeme qui presente des parametres definissant la representation sur le systeme modelise et fonctionne selon deux modes : execution et formation. Un preprocesseur de donnees pretraite des donnees recues en fonction de parametres de pretraitement predefinis et fournit les donnees pretraitees. Ce preprocesseur de donnees comprend une memoire tampon d'entree, concue pour recevoir et stocker les donnees d'entree. Les donnees d'entree peuvent se situer sur differentes echelles de temps. Un dispositif de fusion de temps determine une echelle de temps souhaitee et rapproche les donnees d'entree de facon que toutes les donnees d'entree soient situees sur l'echelle de temps souhaitee. Un dispositif de sortie fournit les donnees rapprochees issues du dispositif de fusion de temps sous forme de donnees pretraitees. Les donnees rapprochees peuvent etre entrees dans la SVM en mode de formation, afin de former la SVM, et/ou en mode d'execution, afin de produire des parametres de commande et/ou des informations de sortie predictives (FIG. 3A, 10, 12, 14, 20, 22).

Legal Status (Type, Date, Text)
Publication 20030731 A1 With international search report.
Examination 20031009 Request for preliminary examination prior to end of 19th month from priority date

4/5/7 (Item 2 from file: 349)

01033020 **Image available**

SYSTEM AND METHOD FOR PRE-PROCESSING INPUT DATA TO A NON-LINEAR MODEL FOR USE IN ELECTRONIC COMMERCE

SYSTEME ET PROCEDE DE PRETRAITEMENT DE DONNEES D'ENTREE EN MODELE NON LINEAIRE DESTINE A ETRE UTILISE DANS LE COMMERCE ELECTRONIQUE

Patent Applicant/Assignee:
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Inventor(s):
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HARTMAN Eric , 12703 Foxhound Cove, Austin, TX 78729, US
Legal Representative:
HOOD Jeffrey C (agent), Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., P.O. Box 398, Austin, TX 78767, US,

Patent and Priority Information (Country, Number, Date):
Patent: WO 200363015 A1 20030731 (WO 0363015)
Application: WO 2003US1520 20030117 (PCT/WO US0301520)
Priority Application: US 200251421 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT SE SI SK TR

h gc c c

(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Main International Patent Class: G06F-015/18
Publication Language: English
Filing Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 34673

English Abstract
A system and method for preprocessing input data to a non-linear model for use in electronic commerce (e-commerce). The non-linear model may include a set of parameters (1422) that define the representation of an e-commerce system. The non-linear model may operate in training (1420) or run-time (1426) mode. A data preprocessor (142) may be provided for preprocessing received data in accordance with predetermined preprocessing parameters and outputting preprocessed data. The data preprocessor may include an input buffer for receiving and storing the input data, where the input data may be on different time scales. A time merge device may select a predetermined time scale and reconcile the input data so that all of the input data are placed on the **same time scale** . An output device may output the reconciled data from the time merge device as preprocessed data. The preprocessed data may then be used as input data to the non-linear model.

French Abstract
L'invention concerne un systeme et un procede de pretraitement de donnees d'entree en modele non lineaire destine a etre utilise dans le commerce electronique. Le modele non lineaire peut comprendre un ensemble de parametres (1422) definissant la representation d'un systeme de commerce electronique. Le modele non lineaire peut fonctionner en mode d'entrainement (1420) ou d'execution (1426). Le systeme et le procede peuvent faire intervenir un dispositif de pretraitement (1412) de donnees destine a pretraiter les donnees recues en fonction de parametres de pretraitement predetermines et a produire des donnees pretraitees. Le dispositif de pretraitement de donnees peut comprendre un tampon d'entree destine a recevoir et a stocker les donnees d'entree, les donnees d'entree pouvant avoir des echelles de temps differentes. Le systeme et le procede peuvent egalement faire intervenir un dispositif de synchronisation permettant de selectionner une echelle de temps predeterminee et de rapprocher les donnees d'entree de facon que toutes les donnees d'entree soient placees sur la meme echelle de temps. Le dispositif et le procede peuvent egalement faire intervenir un dispositif de sortie destine a transmettre les donnees rapprochees du dispositif de synchronisation sous forme de donnees pretraitees. Les donnees pretraitees peuvent ensuite etre utilisees comme des donnees d'entree pour le modele non lineaire.

Legal Status (Type, Date, Text)
Publication 20030731 A1 With international search report.
Examination 20031016 Request for preliminary examination prior to end of 19th month from priority date

4/5/8 (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

h gc c c

01032964 **Image available**

SYSTEM AND METHOD FOR OPERATING A NON-LINEAR MODEL WITH MISSING DATA FOR USE IN ELECTRONIC COMMERCE

SYSTEME ET PROCEDE PERMETTANT DE METTRE EN OEUVRE UN MODELE NON LINEAIRE AVEC DES DONNEES MANQUANTES A UTILISER DANS LE COMMERCE ELECTRONIQUE

Patent Applicant/Assignee:
PAVILION TECHNOLOGIES INC, 11100 Metric Boulevard, Suite 700, Austin, TX 78758, US, US (Residence), US (Nationality)
Inventor(s):
FERGUSON Bruce , 903 Morning View Place, Round Rock, TX 78664, US,
HARTMAN Eric , 12703 Foxhound Cove, Austin, TX 78729, US
Legal Representative:
HOOD Jeffrey C (agent), Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C., P.O. Box 398, Austin, TX 78767, US,

Patent and Priority Information (Country, Number, Date):
Patent: WO 200362952 A2-A3 20030731 (WO 0362952)
Application: WO 2003US1521 20030117 (PCT/WO US03001521)
Priority Application: US 200251598 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT SE SI SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Main International Patent Class: G06F-017/60
Publication Language: English
Filing Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 20166

English Abstract
A system and method for preprocessing input electronic commerce data to a non-linear model for use in an electronic commerce (e-commerce) system. The non-linear model includes parameters that define the representation of the e-commerce system, and operates in two modes: run-time (1426) and training (1420). A data preprocessor (1412) preprocesses received data in accordance with predetermined preprocessing parameters (1422), and outputs preprocessed data. The data preprocessor (1412) includes an input buffer for receiving and storing the input data (1410). The input (1410) data may include one or more outlier values. A data filter detects and removes, and may optionally replace, any outlier values in the input data (1410), generating corrected input data (1410). An output device outputs the corrected data from the data filter as preprocessed data, which may be input to the non-linear model in training mode to train the non-linear model, and/or in run-time mode to generate control parameters and/or predictive output information for the e-commerce system.

French Abstract
L'invention concerne un systeme et un procede permettant de pretraiter des donnees de commerce electronique d'entree dans un modele non lineaire a utiliser dans un systeme de commerce electronique. Le modele non lineaire comprend des parametres definissant la representation du systeme

de commerce electronique et fonctionne selon deux modes: execution et formation. Un preprocesseur de donnees pretraite des donnees recues conformement a des parametres de pretraitement predetermines et emet des donnees pretraitees. Le preprocesseur de donnees comprend un tampon d'entree permettant de recevoir et de stocker les donnees d'entree. Celles-ci peuvent comprendre une ou plusieurs valeurs aberrantes. Un filtre de donnees detecte et elimine et peut eventuellement remplacer une valeur aberrante quelconque dans les donnees d'entree, de maniere a generer des donnees d'entree correctes. Un dispositif de sortie emet les donnees corrigees a partir du filtre de donnees presentees comme des donnees pretraitees, pouvant etre entrees dans le modele non lineaire selon un mode formation permettant de former le modele non lineaire et/ou selon un mode execution permettant de generer des parametres de commande et/ou des informations de sortie predictives destinees au systeme de commerce electronique.

Legal Status (Type, Date, Text)
Publication 20030731 A2 Without international search report and to be republished upon receipt of that report.
Search Rpt 20040129 Late publication of international search report
Republication 20040129 A3 With international search report.
Republication 20040129 A3 Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.
Examination 20040129 Request for preliminary examination prior to end of 19th month from priority date

4/5/9 (Item 4 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

01032954 **Image available**
PRE-PROCESSING INPUT DATA WITH OUTLIER VALUES FOR A SUPPORT VECTOR MACHINE
PRETRAITEMENT DE DONNEES D'ENTREE PRESENTANT DES VALEURS ABERRANTES POUR UNE MACHINE A VECTEUR DE SUPPORT
Patent Applicant/Assignee:
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MEYERTONS HOOD KIVLIN KOWERT & GOETZEL P C (agent), HOOD, Jeffrey C., P.O. Box 398, Austin, TX 78767, US,
Patent and Priority Information (Country, Number, Date):
Patent: WO 200362940 A1 20030731 (WO 0362940)
Application: WO 2003US1372 20030117 (PCT/WO US0301372)
Priority Application: US 200251266 20020118
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
(EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LU MC NL PT SE SK TR
(OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
(EA) AM AZ BY KG KZ MD RU TJ TM

h gc c c

Main International Patent Class: G06E-001/00
International Patent Class: G06F-015/18
Publication Language: English
Filing Language: English
Fulltext Availability:
  Detailed Description
  Claims
Fulltext Word Count: 23487

English Abstract
A system and method for pre-processing input data (10) to a support vector machine (SVM). The SVM is a system model having parameters that define the representation of the system being modeled (26), and operates in two modes: run-time (26) and training (20). A data preprocessor (12) preprocesses (14) received data in accordance with predetermined preprocessing parameters (22), and outputs (41) preprocessed data. The data preprocessor includes an input buffer (62) for receiving and storing the input data (904). The input data may include one or more outlier (907) values (909). A data filter detects and removes (909) any outlier values in the input data, generating corrected input data(910). The filter may optionally replace (911) the outlier values in the input data (904). An output device outputs the corrected data from the data filter as preprocessed data. The corrected data may be input to the SVM in training mode to train the SVM, and/or in run-time mode to generate control parameters and/or predictive output information.

French Abstract
L'invention concerne un systeme et un procede permettant de pretraiter des donnees d'entree (10) destinees a une machine a vecteur de support (SVM). Cette machine a vecteur de support est un modele de systeme comportant des parametres qui definissent la representation du systeme modele (26) et fonctionne dans deux modes : mode d'execution (26) et mode d'entrainement (20). Un processeur de donnees (12) pretraite (14) les donnees recues conformement a des parametres de pretraitement predetermines (22) et produit (41) des donnees pretraitees. Le dispositif de pretraitement de donnees comprend un tampon d'entree (62) destine a recevoir et a stocker ces donnees d'entree (904), lesquelles peuvent comprendre une ou plusieurs valeurs (909) aberrantes (907). Un filtre de donnees detecte et supprime (909) toute valeur aberrante des donnees d'entree, generant ainsi des donnees d'entree corrigees (910). Ce filtre peut eventuellement remplacer (911) les valeurs aberrantes des donnees d'entree (904). Un dispositif de sortie produit les donnees corrigees du filtre de donnees sous forme de donnees pretraitees. Ces donnees corrigees peuvent etre introduites dans la machine a vecteur de support dans le mode d'entrainement afin d'entrainer la machine a vecteur de support et/ou dans le mode d'execution afin de generer des parametres de commande et/ou des informations de sortie predictives.

Legal Status (Type, Date, Text)
Publication 20030731 A1 With international search report.
Examination 20031106 Request for preliminary examination prior to end of 19th month from priority date

4/5/10 (Item 5 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT

00269316 **Image available**
A PREDICTIVE NETWORK WITH LEARNED PREPROCESSING PARAMETERS

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RESEAU PREDICTIF AVEC PARAMETRES DE PRETRAITEMENT APPRIS

Patent Applicant/Assignee:
PAVILLON TECHNOLOGIES INC,
Inventor(s):
KEELER James David,
HARTMAN Eric J ,
O'HARA Steven A,
KEMPF Jill L,
GODBOLE Devendra B

Patent and Priority Information (Country, Number, Date):
Patent: WO 9417489 A1 19940804
Application: WO 94US905 19940125 (PCT/WO US9400905)
Priority Application: US 938218 19930125
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AU CA JP KP AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE
Main International Patent Class: G06F-015/80
Publication Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 13072

English Abstract
A predictive network is disclosed for operating in a runtime mode and in a training mode. The network includes a preprocessor (34') for preprocessing input data in accordance with parameters stored in a storage device (14') for output as preprocessed data to a delay device (36'). The delay device (36') provides a predetermined amount of delay as defined by predetermined delay settings in a storage device (18). The delayed data is input to a system model (26') which is operable in a training mode or a runtime mode. In the training mode, training data is stored in a data file (10) and retrieved therefrom for preprocessing and delay and then input to the system model (26'). Model parameters are learned and then stored in the storage device (22). During the training mode, the preprocess parameters are defined and stored in a storage device (14) in a particular sequence and delay settings are determined in the storage device (18). During the runtime mode, runtime data is derived from a distributed control system (24) and then preprocessed in accordance with predetermined process parameters and delayed in accordance with the predetermined delay settings. The preprocessed data is then input to the system model (26') to provide a predicted output, which is a control output to the distributed control system (24).

French Abstract
On decrit un reseau predictif pouvant fonctionner en mode execution et en mode apprentissage. Le reseau comprend un preprocesseur (34') qui traite au prealable les donnees en entree en fonction des parametres stockes dans un organe de stockage (14') et les envoie sous forme de donnees pretraitees a un organe de retard (36'). Ce dernier produit une quantite determinee de retard, definie par des reglages de retard predetermines contenus dans un organe de stockage (18). Les donnees retardees sont introduites dans un modele du systeme (26') qui peut fonctionner en mode apprentissage ou en mode execution. En mode apprentissage, les donnees d'apprentissage sont stockees dans un fichier de donnees (10) et extraites de ce dernier afin d'etre traitees et retardees avant d'etre introduits dans le modele de systeme (26'). Les parametres du modele sont appris et ensuite stockes dans l'organe de stockage (22). Pendant le mode apprentissage, les parametres de

pretraitement sont definis et stockes dans un organe de stockage (14) selon une sequence determinee et des reglages de retard sont determines dans l'organe de stockage (18). En mode execution, les donnees d'exploitation sont derivees d'un systeme de commande distribue (24) et ensuite pretraitees en fonction des parametres de traitement predetermines et retardees en fonction des reglages de retard predetermines. Les donnees pretraitees sont ensuite introduites dans le modele du systeme (26') pour produire une sortie predite, qui est une commande envoyee au systeme de commande distribue (24).

4/5/11 (Item 6 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

00269309 **Image available**

METHOD AND APPARATUS FOR PREPROCESSING INPUT DATA TO A NEURAL NETWORK

PROCEDE ET APPAREIL DE PRETRAITEMENT DES DONNEES INTRODUCITES DANS UN RESEAU NEURONAL

Patent Applicant/Assignee:
PAVILION TECHNOLOGIES INC,
Inventor(s):
KEELER James David,
HARTMAN Eric J ,
O'HARA Steven A,
KEMPF Jill L,
GODBOLE Devendra B

Patent and Priority Information (Country, Number, Date):
Patent: WO 9417482 A1 19940804
Application: WO 94US910 19940125 (PCT/WO US9400910)
Priority Application: US 938170 19930125
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AU CA JP KP AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE
Main International Patent Class: G06F-015/353
Publication Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 13816

English Abstract

A preprocessing system for preprocessing input data to a neural network includes a training system for training a model (20) on data from a data file (10). The data is first preprocessed in a preprocessor (12) to fill in bad or missing data and merge all the time values on a **common time scale** . The preprocess operation utilizes preprocessing algorithms and time merging algorithms which are stored in a storage area (14). The output of the preprocessor (12) is then delayed in a delay block (16) in accordance with delay settings in storage area (18). These delayed outputs are then utilized to train the model (20), the model parameter is then stored in a storage area (22) during run time, a distributed control system (24) outputs the data to a preprocess block (34) and then preprocesses data in accordance with the algorithms in storage area (14). These outputs are then delayed in accordance with a delay block (36) with the delay settings (18). The output of the delay block (36) comprises inputs to a run time system model (26) which is built to provide a representation of the system in accordance with the model parameters in the storage area (22). A predicted control output or predicted control

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inputs are then generated. The control input is input back to the DCS (24).

French Abstract

Un systeme de pretraitement des donnees en entree dans un reseau neuronal comprend un systeme d'entrainement d'un modele (20) sur des donnees provenant d'un fichier de donnees (10). Ces donnees sont d'abord pretraitees dans un preprocesseur (12) pour completer les donnees defectueuses ou manquantes et fusionner toutes les valeurs de temps en une echelle de temps commune. L'operation de pretraitement utilise des algorithmes de pretraitement et des algorithmes de fusionnement de valeurs temporelles qui sont stockees dans une zone de stockage (14). Les signaux de sortie du preprocesseur (12) sont ensuite retardes dans un bloc de retard (16) en fonction des reglages de retard dans une zone de stockage (18). Ces signaux de sortie retardes sont alors utilises pour entrainer le modele (20), les parametres du modele sont stockes dans une zone de stockage (22) pendant l'execution, un systeme de commande distribue (24) envoie les donnees a un bloc de pretraitement (34), les donnees etant traitees selon les algorithmes contenus dans la zone de stockage (14). Ces signaux de sortie sont ensuite retardes en fonction d'un bloc de retard (36) avec les reglages de retard (18). Les signaux de sortie du bloc de retard (36) comprennent des signaux d'entree dans un modele du systeme d'execution (26) qui est concu pour fournir une representation du systeme selon les parametres du modele dans la zone de stockage (22). Des signaux predits de sortie de commande ou des signaux predits d'entree de commande sont ensuite produits, ces derniers etant reinjectes dans le DCS (24).

4/5/12 (Item 7 from file: 349)

DIALOG(R)File 349:PCT FULLTEXT

00264779 **Image available**

METHOD AND APPARATUS FOR OPERATING A NEURAL NETWORK WITH MISSING AND/OR INCOMPLETE DATA

PROCEDE ET APPAREIL D'EXPLOITATION D'UN RESEAU NEURONAL PRESENTANT DES DONNEES MANQUANTES ET/OU INCOMPLETES

Patent Applicant/Assignee:
PAVILION TECHNOLOGIES INC,
Inventor(s):
KEELER James David,
HARTMAN Eric Jon ,
FERGUSON Ralph Bruce

Patent and Priority Information (Country, Number, Date):
Patent: WO 9412948 A1 19940609
Application: WO 93US11251 19931119 (PCT/WO US9311251)
Priority Application: US 92980664 19921124
Designated States:
(Protection type is "patent" unless otherwise stated - for applications prior to 2004)
AU CA JP KP AT BE CH DE DK ES FR GB GR IE IT LU MC NL PT SE
Main International Patent Class: G06F-015/80
Publication Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 9246

English Abstract

h gc c c

A neural network system is provided that models the system in a system model (12) with the output thereof providing a predicted output. This predicted output is modified or controlled by an output control (14). Input data is processed in a data preprocess step (10) to reconcile the data for input to the system model (12). Additionally, the error resulted from the reconciliation is input to an uncertainty model to predict the uncertainty in the predicted output. This is input to a decision processor (20) which is utilized to control the output control (14). The output control (14) is controlled to either vary the predicted output or to inhibit the predicted output whenever the output of the uncertainty model (18) exceeds a predetermined decision threshold, input by a decision threshold block (22). Additionally, a validity model (16) is also provided which represents the reliability or validity of the output as a function of the number of data points in a given data region during training of the system model (12). This predicts the confidence in the predicted output which is also input to the decision processor (20). The decision processor (20) therefore bases its decision on the predicted confidence and the predicted uncertainty. Additionally, the uncertainty output by the data preprocess block (10) can be utilized to train the system model (12).

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SHOW FILES;DS
File 275:Gale Group Computer DB(TM) 1983-2005/Feb 08
         (c) 2005 The Gale Group
File 621:Gale Group New Prod.Annou.(R) 1985-2005/Feb 08
         (c) 2005 The Gale Group
File 636:Gale Group Newsletter DB(TM) 1987-2005/Feb 08
         (c) 2005 The Gale Group
File  16:Gale Group PROMT(R) 1990-2005/Feb 08
         (c) 2005 The Gale Group
File 160:Gale Group PROMT(R) 1972-1989
         (c) 1999 The Gale Group
File 148:Gale Group Trade & Industry DB 1976-2005/Feb 07
         (c)2005 The Gale Group
File 624:McGraw-Hill Publications 1985-2005/Feb 08
         (c) 2005 McGraw-Hill Co. Inc
File  15:ABI/Inform(R) 1971-2005/Feb 08
         (c) 2005 ProQuest Info&Learning
File 647:CMP  Computer Fulltext 1988-2005/Jan W4
         (c) 2005 CMP Media, LLC
File 674:Computer News Fulltext 1989-2005/Jan W5
         (c) 2005 IDG Communications
File 696:DIALOG Telecom. Newsletters 1995-2005/Feb 07
         (c) 2005 The Dialog Corp.
File 369:New Scientist 1994-2005/Jan W4
         (c) 2005 Reed Business Information Ltd.
File 810:Business Wire 1986-1999/Feb 28
         (c) 1999 Business Wire
File 813:PR Newswire 1987-1999/Apr 30
         (c) 1999 PR Newswire Association Inc
File 610:Business Wire 1999-2005/Feb 08
         (c) 2005 Business Wire.
File 613:PR Newswire 1999-2005/Feb 08
         (c) 2005 PR Newswire Association Inc
File  20:Dialog Global Reporter 1997-2005/Feb 08
         (c) 2005 The Dialog Corp.
File   9:Business & Industry(R) Jul/1994-2005/Feb 07
         (c) 2005  The Gale Group
File 476:Financial Times Fulltext 1982-2005/Feb 08
         (c) 2005 Financial Times Ltd
File 634:San Jose Mercury  Jun 1985-2005/Feb 06
         (c) 2005 San Jose Mercury News
File  47:Gale Group Magazine DB(TM) 1959-2005/Feb 07
         (c) 2005 The Gale group
File 635:Business Dateline(R) 1985-2005/Feb 08
         (c) 2005 ProQuest Info&Learning
File 570:Gale Group MARS(R) 1984-2005/Feb 08
         (c) 2005 The Gale Group
File 387:The Denver Post 1994-2005/Feb 04
         (c) 2005 Denver Post
File 471:New York Times Fulltext 19802005/Feb 08
         (c) 2005 The New York Times
File 492:Arizona Repub/Phoenix Gaz 19862002/Jan 06
         (c) 2002 Phoenix Newspapers
File 494:St LouisPost-Dispatch 1988-2005/Feb 07
         (c) 2005 St Louis Post-Dispatch
File 498:Detroit Free Press 1987-2005/Jan 22
         (c) 2005 Detroit Free Press Inc.
File 631:Boston Globe 1980-2005/Feb 07
         (c) 2005 Boston Globe
File 633:Phil.Inquirer 1983-2005/Feb 05
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        (c) 2005 Philadelphia Newspapers Inc
File 638:Newsday/New York Newsday 1987-2005/Feb 07
        (c) 2005 Newsday Inc.
File 640:San Francisco Chronicle 1988-2005/Feb 08
        (c) 2005 Chronicle Publ. Co.
File 641:Rocky Mountain News Jun 1989-2005/Feb 08
        (c) 2005 Scripps Howard News
File 702:Miami Herald 1983-2005/Feb 04
        (c) 2005 The Miami Herald Publishing Co.
File 703:USA Today 1989-2005/Feb 07
        (c) 2005 USA Today
File 704:(Portland)The Oregonian 1989-2005/Feb 06
        (c) 2005 The Oregonian
File 713:Atlanta J/Const. 1989-2005/Jan 30
        (c) 2005 Atlanta Newspapers

Set     Items   Description
S1     210333   (NONLINEAR OR NON()LINEAR)(1W)(MODEL? ? OR SYSTEM? ?) OR N-
             EURAL?()(NET? ? OR NETWORK? ?) OR AI OR ARTIFICIAL()INTELLIGE-
             NCE
S2    1374439   TIMESCALE? ? OR TIMEBAR? ? OR TIMELINE? ? OR TIME(3N)(SCAL-
             E? ? OR UNIT? ? OR PERIOD? ? OR INTERVAL? ? OR MEASURE? ? OR -
             BAR? ? OR LINE? ? OR SERIES)
S3     108155   (COMMON OR SINGLE OR SINGULAR OR ONE OR MASTER OR TEMPLATE
             OR SAME OR UNIFORM OR CONSISTENT)(2W)S2
S4       1242   S3(15N)(CONFORM? OR RECONCIL? OR ADAPT? OR CONVERT??? OR C-
             ONVERSION OR TRANSLAT? OR TRANSFORM? OR MAP????)
S5       5235   S3(15N)(MERG??? OR COMBIN? OR FUSE? ? OR FUSING OR CHANG???
              OR ADJUST??? OR ADJUSTMENT OR MODIF???? OR MODIFICATION OR A-
             LTER??? OR ALTERATION)
S6      54066   (TRAINING OR "TEST")(1W)(PATTERN? ? OR DATA OR STRING? ? OR
              VECTOR? ?)
S7         20   S4:S5(100N)S1
S8          3   S4:S5(100N)S6
S9      19836   PREFILTER? OR PREPROCESS??? OR PRE()(FILTER??? OR PROCESS?-
             ??)
S10         1   S4:S5(100N)S9
S11        24   S7:S8 OR S10
S12        18   RD (unique items)
?
```

T/3,K/ALL

12/3,K/1 (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)

01196608 SUPPLIER NUMBER: 06076552
Chaos: a tutorial for engineers.
Parker, Thomas S.; Chua, Leon O.
Proceedings of the IEEE, v75, n8, p982(27)
Aug, 1987
ISSN: 0018-9219 LANGUAGE: ENGLISH RECORD TYPE: ABSTRACT

ABSTRACT: The 1980's brought great interest and enthusiasm in **nonlinear systems** research. These were prompted by two factors: the availability of cheap computer power for numerical...

...which may be unpredictable and which exhibit random behavior. As a result of research in **nonlinear systems** , practical techniques for categorizing their steady states, including strange behavior, are available. These methods include: the Poincare' **map** ; Lyapunov exponents; information, correlation, and Lyapunov dimensions; and the reconstruction of attractors from a **single time series** .

12/3,K/2 (Item 1 from file: 636)
DIALOG(R)File 636:Gale Group Newsletter DB(TM)

05753179 Supplier Number: 114411375 (USE FORMAT 7 FOR FULLTEXT)
DTV marketplace.
Broadcast Engineering, v46, n3, pNA
March 1, 2004
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 25372

... EDITING SYSTEM
DVS CLIPSTER
Works with uncompressed material in any resolution - up to 2K - on **one timeline** ; stores video material in its native resolution and format, making pre- **converting** or compressing unnecessary; enables productive real-time editing of up to 2K in RGB 10...

...timeline; output resolution is variable. +49-511-67 80 70; www.dvs.de
Booth: SL4713
NONLINEAR EDITING **SYSTEM**
EVS Broadcast CleanEdit
Nonlinear editing in news configuration allows multiple formats to co-exist within...

12/3,K/3 (Item 2 from file: 636)
DIALOG(R)File 636:Gale Group Newsletter DB(TM)

01256904 Supplier Number: 41335881 (USE FORMAT 7 FOR FULLTEXT)
CAP GEMINI AMERICA UNVEILS AI TOOL FOR DB2 MIGRATION
Report on IBM, v7, n20, pN/A
May 16, 1990

h gc c c g

Language: English Record Type: Fulltext
Document Type: Newsletter; Trade
Word Count: 234

The new system, **Artificial Intelligence** Migration (AIM), is a tool-driven service that lets users move to relational technology but...

...investments in Cullinet Soft-ware's IDMS hierarchical database.
Previously programmers had to use a **translator** program to make **changes** to a source program on a **one** -□line□ -at-a-□time□basis. AIM deciphers the program logic and context associated with database access and intelligently decides...

...conversion engineering.
CAP re-engineered a DB2 definition to drive the transformation of source programs. **AI** techniques transform the program by analyzing the program's database usage and navigational logic, Ross...

12/3,K/4 (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

10032169 Supplier Number: 90932329 (USE FORMAT 7 FOR FULLTEXT)
Hearing aid physical fit: the next revolution?
Fabry, David
The Hearing Journal, v55, n8, p46(3)
August, 2002
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 1838

... digital acoustics are now moving in directions that are nor possible with analog hearing aids: **neural networks** , binaural processing, and **adaptive** beamforming networks.
During the **same time period** , less attention has been focused on earmold/shell fabrication, an area that, while less glamorous...

12/3,K/5 (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

03773871 Supplier Number: 45365928 (USE FORMAT 7 FOR FULLTEXT)
COMMERCIAL TELEVISION: DEAD OR ALIVE? A STATUS REPORT ON NIELSEN'S PASSIVE PEOPLE METER(*)
Journal of Advertising Research, pRC5
March, 1995
Language: English Record Type: Fulltext
Document Type: Magazine/Journal; Professional
Word Count: 3895

... people are - it only knows that someone is still there.
When the tracking system is **combined** with face recognition, so that the unknown person can be positively identified at least for **one** point in **time** during the **period** they remained in the area in front of the TV, the identity can be credited...

...the room requires the application of computer logic rules, which we have referred to as **artificial intelligence** software. These edit rules and

processing software are an integral part of the passive people...

12/3,K/6 (Item 3 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

01143157 Supplier Number: 41295652
CAP GEMINI AMERICA ANNOUNCES IDMS TO DB2 MIGRATION SERVICE BASED ON AI TOOL
News Release, p1
April 25, 1990
Language: English Record Type: Abstract
Document Type: Magazine/Journal; Trade

ABSTRACT:
...the existing investment in IDMS-coded software. The only approach previously available was through a **translator** program used to make basic **changes** to a source program on a **one** -□line□ -at-a-□time□basis. Using□AI□ techniques, AIM deciphers the programs logic and context associated with database access and intelligently decides...

12/3,K/7 (Item 1 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB

16678601 SUPPLIER NUMBER: 111856311 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Forecasting with leading economic indicators--a neural network approach.
Jagric, Timotej
Business Economics, 38, 4, 42(13)
Oct, 2003
ISSN: 0007-666X LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 7009 LINE COUNT: 00633

... layer with the same characteristics as already present neurons. This allowed us to forecast two **time series** at the **same time** . The estimation procedure was therefore **changed** .
* We tested the network and compared the results with the results from major testing. If...

...network designs (see Table 4 for the list of tested network designs), we selected a **neural network** , which can be represented with the following equations:
(4) y = (f.sup.3,1) ((w...

12/3,K/8 (Item 2 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB

16537411 SUPPLIER NUMBER: 111400748 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Media 100 HD for under $8K. (Cutting Edge) (Brief Article)
Broadcasting & Cable, 133, 50, 34(1)
Dec 15, 2003
DOCUMENT TYPE: Brief Article ISSN: 1068-6827 LANGUAGE: English
RECORD TYPE: Fulltext
WORD COUNT: 144 LINE COUNT: 00013

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TEXT:
Nonlinea r-editing- **system** manufacturer Media 100 has introduced an HD version of its system priced less than $8...

...resolution independence, permitting editors to mix and match HD and SD source material in the **same timeline** , a first for personal computers. HDX also provides format **conversion** , enabling Media 100 HD to **convert** from any HD format to any HD format, from any SD format to any HD...

12/3,K/9 (Item 3 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB

11363108 SUPPLIER NUMBER: 55830809 (USE FORMAT 7 OR 9 FOR FULL TEXT)
The supply of infants relinquished for adoption: did access to abortion make a difference?
Gennetian, Lisa A.
Economic Inquiry, 37, 3, 412(2)
July, 1999
ISSN: 0095-2583 LANGUAGE: English RECORD TYPE: Fulltext; Abstract
WORD COUNT: 9969 LINE COUNT: 00833

... relinquished or that further restrictions on abortion would increase the supply of infants relinquished. The **changing** opportunity costs of women over this **same time period** may provide a better explanation than the direct effects of public policy. In both the...

...to continue with a pregnancy, give birth and relinquish, or keep the infant.
APPENDIX TABLE **AI**
Data Definitions and Sources
The number of infants relinquished is the total number of domestic...

12/3,K/10 (Item 4 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB

07924634 SUPPLIER NUMBER: 17042828 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Commercial television: dead or alive? A status report on Nielsen's passive people meter.(Research Currents)
Cook, Barry
Journal of Advertising Research, v35, n2, pRC-5(6)
March 13, 1995
ISSN: 0021-8499 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 4160 LINE COUNT: 00310

... people are - it only knows that someone is still there.
When the tracking system is **combined** with face recognition, so that the unknown person can be positively identified at least for **one** point in **time** during the **period** they remained in the area in front of the TV, the identity can be credited...

...the room requires the application of computer logic rules, which we have referred to as **artificial intelligence** software. These edit rules and processing software are an integral part of the passive people...

12/3,K/11 (Item 5 from file: 148)

DIALOG(R)File 148:Gale Group Trade & Industry DB

04582396 SUPPLIER NUMBER: 09013569 (USE FORMAT 7 OR 9 FOR FULL TEXT)
Testing procedures significant to casting quality.
Mizzi-Krysiak, Mary Beth; Pedicini, Louis J.
Modern Casting, v80, n4, p39(3)
April, 1990
ISSN: 0026-7562 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 1964 LINE COUNT: 00159

... difficult to control the time variable and sand properties. Moisture, friability, even strength characteristics can **change** with time. Even if the tests are taken at the **same line** point, the **time** elapsed since mold preparation and testing will vary with other factors, such as line downtime...

...composition or some other production factor Laboratory data should determine sand changes. Correlation of Sand **Test Data**
Mold strength, hardness and permeability tests can be performed on the mold. Mold strength tests...

12/3,K/12 (Item 6 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB

04118369 SUPPLIER NUMBER: 07989159 (USE FORMAT 7 OR 9 FOR FULL TEXT)
MHP agrees to sell assets to R&D. (MHP Machines Inc.; R&D Manufacturing Co.)
Irving, Robert R.
Metalworking News, v16, n752, p6(1)
Sept 18, 1989
ISSN: 0891-4036 LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
WORD COUNT: 487 LINE COUNT: 00037

... machine.
MPH introduced its CNC Hydra-Path III controller about four years ago. In that **same time period** the company **changed** over from hydraulic to DC electric drives. Other innovations included a knee-type machining center...

...88 in Chicago, MHP introduced Quickcam (MN, Sep. 12, 1988), a CAD/CAM system using **artificial intelligence** from MicroData Technology Inc., Amherst, N.H.

12/3,K/13 (Item 1 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)

01754364 04-05355
Weeding out input errors
Anonymous
Mechanical Engineering v120n11 PP: 16-18 Nov 1998
ISSN: 0025-6501 JRNL CODE: MEG
WORD COUNT: 367

...TEXT: tool, includes new Win"dows-compliant data entry screens and consolidates Algor's most popular **preprocessing** functions, including

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Houdini's CAD interfacing and meshing capabilities. With Superdraw III's new unit system, engineers are able to verify or **change** an FEA model's **units** at any **time** . A□single□ -□unit□system, such as S.I. or English, can be selected with one mouse click. All...

12/3,K/14 (Item 1 from file: 613)
DIALOG(R)File 613:PR Newswire

0001354390 I71A5DE6018C511D9B0D683C7F34AAC5D (USE FORMAT 7 FOR FULLTEXT)
Captaris Embeds Pervasive Data Integration Solutions Accelerating Information Delivery for Captaris Interchange
PR Newswire
Wednesday, October 6, 2004 T11:00:00Z
JOURNAL CODE: PR LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT
DOCUMENT TYPE: NEWSWIRE
WORD COUNT: 1,082

...integration products feature easy-to-use visual design tools for developers to rapidly build and **test** **data** integration processes, a rich development environment for embedding integration solutions into applications, and the functionality...

...Pervasive's easy-to-use, cost-effective integration technology, ISVs and systems integrators can easily **transform** their applications into "integration-ready" solutions that **scale** from **one** -□time□data migrations to ongoing application integration within and beyond organizational boundaries. For more information on...

12/3,K/15 (Item 1 from file: 494)
DIALOG(R)File 494:St LouisPost-Dispatch

05570663
CONTROL SYSTEM COULD SAVE LIVES
ST. LOUIS POST DISPATCH (SL) - MONDAY November 5, 1990
By: Robert Sanford
Of the Post-Dispatch Staff
Edition: FIVE STAR Section: MONDAY'S BUSINESS SECTION Page: 1
Word Count: 1,311

...becomes a new model.

David A. White, a neurocomputing engineer, explained some basics of a **neural** **network** . The most advanced control system, he said, is the human brain. The structure can be...

...s ability to process information in parallel such as seeing a dog and at the **same** **time** hearing it **bark** . . .," he said. "The brain can□adapt□ to sudden **changes** , as it does when a person drives in heavy traffic, or it can plan strategies...

...from point A to point B . . .
"Similar to the structure of the brain, an artificial **neural** **network** consists of multiple layers of processing elements . . . These networks can be trained through multiple examples...

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12/3,K/16 (Item 1 from file: 641)
DIALOG(R)File 641:Rocky Mountain News
(c) 2005 Scripps Howard News. All rts. reserv.

12500000
NFL THIS WEEK TEAMS, THE LOWDOWN, NUMBERS GAME, TIPPING THE SCALES
Rocky Mountain News (RM) - FRIDAY, November 12, 2004
By: Richard Lord, Rocky Mountain News
Edition: Final Section: Football Weekend Page: 9F
Word Count: 1,370

TEXT:
... Chad Pennington against the nasty Ravens defense. His past (30 TDs, 36 INTs with Dallas) **suggests** he's not equal to the task. The Jets' run defense looked vulnerable against Buffalo...

... the tough running of Shaun Alexander, the Rams have lost two in a row, allowing **71 points** in **the** process. □24sacks□ of Rams QB□Marc□ Bulger, including five last week in a loss to New England. * A likely shootout boils down to two questions: Can **the** Rams **protect Bulger** ? Can St. Louis stop Alexander? Toss **a coin** ! Tampa **Bay** (□3□ -□5□) at Atlanta (6-2) 11 a.m. Sunday * Certainly many Broncos fans find it hard...

12/3,K/17 (Item 1 from file: 710)
DIALOG(R)File 710:Times/Sun.Times(London)
(c) 2005 Times Newspapers. All rts. reserv.

14122220
PASTE FOR EASY READ;DR KEYBOARD /BY
Times of London (TL) - Monday, May 1, 2000
Section: Features
Word Count: 722

... on offer? We don't need the second analogue line, just the two main ones.

AI think you're a little confused about what the Home Highway system gives you. It...

... telephone line into two digital (ISDN) telephone lines, which you can use as two ISDN **lines** at **one time** to give you a 128kbps connection to your Internet Service Provider (ISP) or as a **combination** of digital and analogue telephone lines so that you could, for example, maintain a 64kbps ...

12/3,K/18 (Item 2 from file: 710)
DIALOG(R)File 710:Times/Sun.Times(London)
(c) 2005 Times Newspapers. All rts. reserv.

12822099
ABOUT THE BLUES;ARTS;POP;MUSIC
Times of London (TL) - Tuesday, November 18, 1997
By: Nigel Williamson
Section: Home news
Word Count: 338

... on a series of high and brittle solos, picking out the simple and insistent bass **lines** at the **same time** . The slow blues of□Ai□Du

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perfectly illustrated the music's journey as Ali **fused** John Lee Hooker's metronomic style with passionate African vocals.

He switched to an acoustic...
?

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